Structural and Flow-Sensitive Types for Whiley

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Abstract

Modern statically typed languages require variables to be declared with a single static type, and that subtyping relationships between used-defined types be made explicit. This contrasts with dynamically typed languages, where variables are declared implicitly, can hold values of different types at different points and have no restrictions on flow (leading to ad-hoc and implicit subtyping).

We present the flow-sensitive and structural type system used in the Whiley language. This permits variables to be declared implicitly, have multiple types within a function, and be retyped after runtime type tests. Furthermore, subtyping between user-defined types is implicit, based purely on structure. The result is a statically-typed language which, for the most part, has the look and feel of a dynamic language. The typing algorithm operates in a fashion similar to dataflow analysis. Widening must be applied to ensure termination although, surprisingly, there is no loss of precision. We formalise Whiley's type system and operational semantics, and give proofs of termination and soundness.

1   Introduction

Statically typed programming languages lead to programs which are more efficient and where errors are easier to detect ahead-of-time [1, 2]. Static typing forces some discipline on the programming process. For example, it ensures at least some documentation regarding acceptable function inputs is provided. In contrast, dynamically typed languages are more flexible in nature which helps reduce overheads and increase productivity [3, 4, 5, 6]. Indeed, recent times have seen a significant shift towards dynamically typed languages [7].

Numerous attempts have been made to bridge the gap between static and dynamic languages. Scala [8], C#3.0 [9], OCaml [10] and, most recently, Java 7 all employ local type inference (in some form) to reduce syntactic overhead. Techniques such as gradual typing [11, 12], soft typing [1, 13] and hybrid typing [14] enable a transitory position where some parts of a program are statically typed, and others are not. Alternatively, global type inference can be used (in some situations) to reconstruct types "after the fact" for programs written in dynamic languages [15, 16].

Such approaches focus on working around static type systems, rather than rethinking the way static typing is done. The prevailing view remains that each variable in a static type system must have exactly one declared type and that *nominal* — rather than *structural* — typing is preferred. And yet, these choices go against the notion of a dynamic language, where variables can be assigned arbitrarily and can flow unimpeded by cumbersome, rigid type hierarchies.

We present the type system used in Whiley, a statically-typed language targeting the JVM. Whiley takes a novel approach to typing, which we refer to as *flow-sensitive typing*. This comes close to giving the flexibility of a dynamic language, with the guarantees of a statically typed language. More specifically, flow-sensitive typing offers improved error handling, reduced syntactic overhead and greater opportunity for code reuse. The technique is adopted from flow-sensitive program analysis (e.g. [17, 18, 19]), and allows variables to have different types at different points and be declared implicitly, based on the type of assigned expressions. Furthermore, unlike the majority of statically typed languages, Whiley employs structural — rather than nominal — typing. This frees programmers from the burden of developing static type hierarchies up front, allowing them to expose subtyping relationships retroactively.

Finally, an open source implementation of Whiley is freely available from `http://whiley.org` and details Whiley's JVM implementation can be found here [20].

1.1 Flow-Sensitive Types

The following demonstrates Whiley's flow-sensitive types:

```
define Circle as {int x, int y, int r}
define Rect as {int x, int y, int w, int h}
define Shape as Circle | Rect


real area(Shape s):
    if s is Circle:
        return PI * s.r * s.r
    else:
        return s.w * s.h
```

A `Shape` is either a `Rect` or a `Circle` (which are both record types). The type test "s `is` Circle" (similar to an `instanceof` test in Java) determines whether s is a `Circle` or not. Unlike Java, Whiley automatically retypes s to have type `Circle` (resp. `Rect`) on the true (resp. false) branches of the `if` statement. There is no need to explicitly cast s to the appropriate `Shape` before accessing its fields.

Statically typed languages typically require variables be explicitly declared. Compared with dynamically typed languages, this is an extra burden for the programmer, particularly when a variable's type can be inferred from assigned expression(s). In Whiley, local variables are never explicitly declared, rather they are declared by assignment:

```
int average([int] items):
    v = 0
    for i in items:
        v = v + items[i]
    return v / |items|
```

Here, `items` is a list of `int`s, whilst `|items|` returns its length. Variable v accumulates the sum of all elements in the list, and is declared by the assignment "v = 0". Since 0 has type `int`, v has type `int` after the assignment.

1.2 Structural Subtyping

Statically typed languages, such as Java, employ nominal typing for recursive data types. This results in rigid hierarchies which are often difficult to extend [21]. In contrast, Whiley employs *structural subtyping* of records [22] to give greater flexibility. For example, the following defines a `Border` record:

```
define Border as {int x,int y,int w,int h}
```

Any instance of `Border` has identical structure to an instance of `Rect`. Wherever a `Border` is required, a `Rect` can be provided and vice-versa — even if the `Border` definition was written long after the `Rect`, and even though no explicit connection is made between `Rect` and `Border`.

The focus on structural, rather than nominal, types in Whiley is also evident in the way instances are created:

```
bool contains(int x, int y, Border b):
    ....


bool example(int x, int y):
    b = {x: 1, y: 2, w: 10, h: 3}
    return contains(x,y,b)
```

Here, function `example()` creates a record instance with fields x, y, w and h, and assigns it to variable b. Despite not being associated with a name, such as `Border` or `Rect`, it can be freely passed into functions expecting such types, since they have identical *structure*.

2 Language Overview

Figure 1 provides a simple implementation of expressions, along with code for evaluating them. The types `Expr` and `Value` are algebraic data types, with the latter defining the set of allowed values. Type `Op` is an enumeration, whilst `BinOp` and `ListAccess` are records which form part of `Expr`. Parameter env is a map from variables to `Value`s. Finally, `null` is used as an error condition to indicate a "stuck" state (i.e. the evaluation cannot proceed).

The code in Figure 1 makes extensive use of runtime type tests to distinguish different expression forms (e.g. "e `is int`"). These work in a similar fashion to Java's `instanceof` operator, with one important

```
1  define Var as string
2  define Op as { ADD, SUB, MUL, DIV }
3  define BinOp as { Op op, Expr lhs, Expr rhs }
4  define ListAccess as { Expr lhs, Expr rhs }
5
6  define Value as int | [Value] | null
7
8  define Expr as int | Var | BinOp | [Expr] | ListAccess
9
10  Value eval(Expr e, {Var→Value} env):
11      if e is int:
12          return e
13      else if e is Var && e in env:
14          // look up variable's value
15          return env[e]
16      else if e is BinOp:
17          // evaluate left and right expressions
18          lhs = eval(e.lhs,env)
19          rhs = eval(e.rhs,env)
20          // sanity check
21          if !(lhs is int && rhs is int):
22              return null // stuck
23          // evaluate result
24          switch e.op:
25              case ADD:
26                  return lhs + rhs
27              case SUB:
28                  return lhs - rhs
29              case MUL:
30                  return lhs * rhs
31              case DIV:
32                  if rhs != 0:
33                      return lhs / rhs
34      else if e is ListAccess:
35          // evaluate src and index expressions
36          src = eval(e.lhs,env)
37          index = eval(e.rhs,env)
38          // santity check
39          if src is [Value] && index is int
40              && index >= 0 && index < |src|:
41              return src[index]
42      else if e is [Expr]:
43          lv = []
44          // evaluate items in list constructor
45          for i in e:
46              v = eval(i,env)
47              if v == null:
48                  return v
49              else:
50                  lv = lv + [v]
51          return lv
52      // some kind of error occurred, so propagate upwards
53      return null
```

Figure 1: Whiley code for a simple expression tree and evaluation function. This makes extensive use of type tests, both for distinguishing expressions and error handling. Flow-sensitive typing greatly simplifies the code, which would otherwise require numerous unnecessary casts.

difference: they operate in a flow-sensitive fashion and automatically *retype* variables after the test. As an example, consider the type test "e **is int**" on Line 11. On the true branch, variable e is automatically retyped to have type **int**. Likewise, on the false branch, e is now known *not* to have type **int** (and any attempt to retest this yields a compile-time error).

Figure 1 also employs runtime type tests to identify and propagate errors. For example, having evaluated the left- and right-hand sides of a BinOp, we check on Line 21 that both are **int** values (i.e. not list values or **null**). After the check, Whiley's flow-sensitive type system automatically retypes both lhs and rhs to **int**. For ListAccess expressions, we check on Line 39 that src is a list value, and that index is an **int**. The latter is achieved with "index **is int**". As src is retyped within the condition itself, the subsequent use of |src| on Line 40 is type safe.

Implementing our expression language in a statically-typed language, such as Java, would require code that was more cumbersome, and more verbose than that of Figure 1. One reason for this is that, in languages like Java, variables must be *explicitly* retyped after instanceof tests. That is, we must insert casts to update the types of tested variables and, since variables can have only one type in Java, introduce temporary variables to hold these new types. For example, after a test "e instanceof BinOp" we must introduce a new variable, say r, with type BinOp and assign e to r using an appropriate cast. A Java implementation would also (most likely) break up the test on Line 39, since it would otherwise need two identical casts (one inside the condition for |src|, and one on the true branch for src[index]).

In an object-oriented language, such as Java, a direct conversion of Figure 1 might not be optimal. Instead, the *visitor pattern* [23] can be used to distinguish different expression forms. Using the visitor pattern reduces the amount of explicit retyping required. This is because the different expression forms are explicitly given as parameters to the visitor methods. However, using the visitor pattern is a heavyweight solution which is not suitable in all situations. In particular, it would not eliminate all forms of explicit retyping from Figure 1. In this case, explicit variable retyping will still be required to properly handle the different values returned from eval(). For example, to check BinOps are evaluated on **int** operands (Line 21), and that src gives a list and index an **int** (Line 39).

2.1 Value Semantics

In Whiley, all compound structures (e.g. lists, sets, and records) have *value semantics*. This means they are passed and returned by-value (as in Pascal, MATLAB or most functional languages). But, unlike functional languages (and like Pascal), values of compound types can be updated in place.

Value semantics implies that updates to a variable only affects that variable, and that information can only flow out of a function through its return value. Whiley has no general, mutable heap comparable to those found in object-oriented languages. Consider:

```
int f([int] xs):
    ys = xs
    xs[0] = 1
    ...
```

The semantics of Whiley dictate that, having assigned xs to ys as above, the subsequent update to xs does not affect ys. Arguments are also passed by value, hence xs is updated inside f() and this does not affect f's caller. That is, xs is not a *reference* to a list of **int**; rather, it *is* a list of **int**s and assignments to it do not affect state visible outside of f().

Whilst this approach may seem inefficient, a variety of techniques exist (e.g. reference counting) to ensure efficiency (see e.g. [24, 25, 26]). Indeed, the underlying implementation does pass compound structures by reference and copies them only when absolutely necessary.

2.1.1 Structural Updates.

In a dynamic language, lists and records can be updated at will. However, static type systems normally require updates to respect the element or field type in question. For example, assigning a float to an element of an **int** array is not permitted in Java. To work around this, programmers typically either clone the structure in question, or "break" the type system using casting (or similar).

Updates to list elements and record fields are always permitted in Whiley. For example:

```
define Point as {int x, int y}

[int|Point] insert(int i, Point p, [int] xs):
    xs[i] = p
    return p
```

The type of xs is updated to [**int**|Point] after xs is assigned as it now contains a Point.

The ability to update the types of records and lists is possible because compound structures (e.g. lists, sets, records, etc) have value semantics in Whiley. Such updates would be unsafe if aliasing were permitted as, for example, callers of insert() might hold aliases to xs and would expect its elements to be **int**s.

2.2 Structural Types

Statically typed languages, such as Java, employ nominal typing for recursive data types. This results in rigid hierarchies which are often difficult to extend [21]. In contrast, Whiley employs *structural subtyping* of records [22] to give greater flexibility.

Suppose we wish to extend our expressions from Figure 1 with assignment statements. A common issue arises as the left-hand side of an assignment is a restricted form of expression, often called an *lval*. In a language like Java, we can capture this nicely using interfaces:

```
interface Expr { ... }
interface LVal { ... }
class ListAccess implements Expr,LVal { ...}
class Var implements Expr,LVal { ... }
class Int implements Expr { ... }
```

However, suppose the code for expressions was part of an existing library, and we are trying to add statements after the fact. In a language like Java, this presents a problem as we cannot retroactively insert the necessary LVal interface to Var and ListAccess.

In Whiley, adding the notion of an LVal is easy to do retroactively because of structural subtyping:

```
define LVal as Var | ListAccess
define Assign as {LVal lhs, Expr rhs}

Expr parseExpression():
    ...


null|Assign parseAssign():
    le = parseExpression()
    match(":=")
    re = parseExpression()
    if le is LVal:
        return {lhs: le, rhs: re}
    else:
        return null // syntax error
```

Here, LVal is implicitly a subtype of Expr — i.e. there is no need for an explicit declaration of this, as would be required in Java. That is, they can be defined entirely separately from each other (e.g. in different files, packages or entirely separate programs) — and yet, LVal remains a subtype of Expr.

2.2.1 Structural Subtyping.

Whiley permits subtyping between recursive structural types.

```
define Link as {int data, LinkedList next}
define LinkedList as null | Link
define OrderedList as null | {
  int data, int order, OrderedList next
}


int sum(LinkedList l):
    if l is null:
        return 0
    else:
        return l.data + sum(l.next)
```

Here, we have defined a standard linked list and a specialised "ordered" list where order < next.order for each node (see e.g. [27]). Whiley type checks this function by showing that OrderedList is a structural subtype of LinkedList — despite this relationship not being identified explicitly in the program. Type checking in the presence of recursive structural types is a well-known and challenging problem [28, 29, 30] which is further compounded in Whiley by the presence of flow-sensitive reasoning.

2.3 Flow-Sensitive Types

We now examine Whiley's flow-sensitive type system in more detail, and identify several ways in which it leads to improved code quality.

Error Handling. Nullable references have proved a significant source of error in e.g. Java [31]. The issue is that, in such languages, one can treat *nullable* references as though they are *non-null* references [32]. Many solutions have been proposed which distinguish these two forms using static type systems (e.g. [33, 34, 35, 36, 37, 38, 39, 40]).

Whiley's flow-sensitive type system lends itself naturally to handling this problem because it supports *union types* (e.g. [41, 42]). These allow variables to hold values from different types, rather than just one type. For example:

```
null|int indexOf(string str, char c):
   ...

[string] split(string str, char c):
    idx = indexOf(str,c)
    // idx has type null|int
    if idx is int:
        // idx now has type int
        below = str[0..idx]
        above = str[idx..]
        return [below,above]
    else:
        // idx now has type null
        return [str] // no occurrence
```

Here, `indexOf()` returns the first index of a character in the string, or **null** if there is none. The type **null**|**int** is a union type, meaning it is either an **int** or **null**.

In the above example, Whiley's flow-sensitive type system seamlessly ensures that **null** is never dereferenced. This is because the type **null**|**int** cannot be treated as an **int**. Instead, one must first check it *is* an **int** using a type test, such as "idx **is int**". Whiley automatically retypes idx to **int** when this is known to be true, thereby avoiding any awkward and unnecessary syntax (e.g. a cast as required in e.g. [43, 38]).

Code Reuse. Whiley's flow-sensitive type system can expose greater opportunities for code reuse:

```
1 {string} usedVariables(Expr e):
2     if e is Var:
3         return {e}
4     else if e is BinOp || e is ListAccess:
5         l = useVariables(e.lhs)
6         r = useVariables(e.rhs)
7         return l + r   // set union
8     else if e is [Expr]:
9         ...
10    else:
11        return {}
```

On Line 5, variable e has type `BinOp|ListAcccess`. The use of `e.lhs` at this point is type safe, since we can perform operations common to all types of a union and, in particular, unions of records expose common fields (similar to a *common initial sequence* for `union`s of `struct`s in C [44, §6.3.2.3]).

In languages like Java, exploiting code reuse in this way requires careful planning, as common types must be explicitly related in the class hierarchy. In contrast, Whiley's flow-sensitive type system lets us exploit opportunities for code reuse in an ad-hoc fashion, as and when they occur.

3 Type System

We formalise the Whiley language using a core calculus which, in the spirit of Featherweight Java [45], we call *Featherweight Whiley (FW)*. The following gives a syntactic definition of types in FW, where overbar (e.g. $\overline{T}$) indicates indicates a list of items numbered consecutively (e.g. $T_1, \ldots, T_n$):

$$T ::= \texttt{any} \mid \texttt{void} \mid \texttt{null} \mid \texttt{int} \mid \overline{T} \rightarrow T \mid [T] \mid \{\, \overline{T\,n}\, \} \mid T_1 \vee T_2 \mid \mu X.T \mid X$$

Here, **void** and **any** represent $\bot$ and $\top$; lists are given by $[T]$; and, $\{\overline{T\,n}\}$ represents records with one or more fields. The union $T_1 \vee T_2$ is a type whose values are in T_1 *or* T_2. Types are additionally restricted to being *contractive* [29]. This prohibits types of the form $\mu X.X$ and $\mu X.(X \vee \ldots)$ and is necessary to ensure a strong connection with regular trees [32].

FW follows the *equi-recursive* approach to dealing with recursive types [32]. That is, we do not distinguish between recursive types and their unfoldings. This simplifies FW's formalism, as one does not need an explicit *unfold* operator. Any implementation of FW, however, must address the fact that two equivalent types can have distinct representations on the machine (analogous to the graph isomorphism problem). We return to discuss this in §3.4.

3.1 Semantic Interpretation

To better understand the meaning of types in FW, it is helpful to give a *semantic interpretation* (following e.g. [46, 47, 48]). The aim is to give a set-theoretic model where subtype corresponds to subset. Whilst our model ties very closely with the semantics of FW, it is simplified to avoid a circular definition between types and semantics [48]. This is often referred to as "bootstrapping" the subtype relation, and affects only function values — which we model as sets of parameter and return values.

The following defines the language of values in our model:

$$V ::= \texttt{null} \mid i \mid [\overline{V}] \mid \{\, \overline{n : V}\, \} \mid (\overline{V}_1, \ldots, \overline{V}_n) \rightarrow \overline{V}$$

Here, $i \in \mathcal{I}$ represents integer values, whilst $(\overline{V}_1, \ldots, \overline{V}_n) \rightarrow \overline{V}$ is our model of functions used to bootstrap the subtyping relation (note the abuse of notation here where $\overline{V}$ indicates a set not a list — but, this should always be clear in context). Each function value consists of sets of parameter and return values which identify those values it can accept in each parameter position, and which it may return. There is no explicit connection between individual parameter values and the corresponding return value, as this level of detail is unnecessary.

Definition 1 (Type Acceptance) *A type* T *accepts a value* V, *denoted by* $T \models V$, *defined as follows:*

$$
\begin{aligned}
\texttt{any} &\models V \\
\texttt{null} &\models \texttt{null} \\
\texttt{int} &\models i && \textit{if } i \in \mathcal{I} \\
[T] &\models [\overline{V}] && \textit{if } \overline{T \models V} \\
\{T_1\,n_1, \ldots, T_n\,n_n\} &\models \{n_1 : V_1, \ldots, n_m : V_m\} && \textit{if } n < m, \overline{T \models V} \\
T_1 \vee T_2 &\models V && \textit{if } T_i \models V, i \in \{1, 2\} \\
\overline{T} \rightarrow T &\models (\overline{V}_1, \ldots, \overline{V}_n) \rightarrow \overline{V} && \textit{if } \overline{V} \models T, \overline{V}_1 \models T, \ldots, \overline{V}_n \models T
\end{aligned}
$$

Here, $\overline{V} \models T$ *denotes* $\neg \exists V.[T \models V \wedge V \notin \overline{V}]$. *In other words, every value accepted by* T *is in the set* $\overline{V}$.

Definition 2 (Type Subset) *We take* $T_1 \models T_2$ *to denote* $\forall V.[T_1 \models V \Longrightarrow T_2 \models V]$. *In other words, that the set of values accepted by* T_1 *is a subset of those accepted by* T_2.

We are essentially viewing types as finite tree automatons which accept conforming trees (i.e values). The rule for accepting function values may seem somewhat strange. However, consider this example:

```
int f(any x):
    return 1

int g(int(int) fp):
    return fp(123)
```

Here, fp is a variable of type $\texttt{int} \rightarrow \texttt{int}$, whilst function f() has type $\texttt{any} \rightarrow \texttt{int}$. Intuitively, g(&f) should be a syntatically correct expression and, hence, $\texttt{any} \rightarrow \texttt{int}$ must subtype $\texttt{int} \rightarrow \texttt{int}$ (as is normal). Essentially, the type $\texttt{int} \rightarrow \texttt{int}$ will accept any function value which is prepared to accept any **int** value.

Subtyping:

$$\frac{}{\mathrm{T} \leq \mathrm{T} \mid \mathcal{C}} \qquad \frac{\{\mathrm{T_1} \leq \mathrm{T_2}\} \subseteq \mathcal{C}}{\mathrm{T_1} \leq \mathrm{T_2} \mid \mathcal{C}} \qquad \text{(S-REFLEX, S-INDUCT)}$$

$$\frac{}{\mathtt{void} \leq \mathrm{T} \mid \mathcal{C}} \qquad \frac{}{\mathrm{T} \leq \mathtt{any} \mid \mathcal{C}} \qquad \text{(S-VOID, S-ANY)}$$

$$\frac{\mathrm{T_1} \leq \mathrm{T_2} \mid \mathcal{C} \cup \{[\mathrm{T_1}] \leq [\mathrm{T_2}]\}}{[\mathrm{T_1}] \leq [\mathrm{T_2}] \mid \mathcal{C}} \qquad \text{(S-LIST)}$$

$$\frac{\begin{array}{c} \mathtt{n} < \mathtt{m} \\ \mathrm{T_1} = \{\mathrm{T_3}\ \mathtt{f_3}, \ldots, \mathrm{T_m}\ \mathtt{f_m}\} \\ \mathrm{T_2} = \{\mathrm{T_3'}\ \mathtt{f_3}, \ldots, \mathrm{T_n'}\ \mathtt{f_n}\} \\ \mathrm{T_3} \leq \mathrm{T_3'} \mid \mathcal{C} \cup \{\mathrm{T_1} \leq \mathrm{T_2}\} \\ \cdots \\ \mathrm{T_n} \leq \mathrm{T_n'} \mid \mathcal{C} \cup \{\mathrm{T_1} \leq \mathrm{T_2}\} \end{array}}{\mathrm{T_1} \leq \mathrm{T_2} \mid \mathcal{C}} \qquad \text{(S-REC)}$$

$$\frac{\begin{array}{c} \overline{\mathrm{T}} \geq \overline{\mathrm{S}} \mid \mathcal{C} \cup \{\overline{\mathrm{T}} {\rightarrow} \mathrm{T} \leq \overline{\mathrm{S}} {\rightarrow} \mathrm{S}\} \\ \mathrm{T} \leq \mathrm{S} \mid \mathcal{C} \cup \{\overline{\mathrm{T}} {\rightarrow} \mathrm{T} \leq \overline{\mathrm{S}} {\rightarrow} \mathrm{S}\} \end{array}}{\overline{\mathrm{T}} {\rightarrow} \mathrm{T} \leq \overline{\mathrm{S}} {\rightarrow} \mathrm{S} \mid \mathcal{C}} \qquad \text{(S-FUN)}$$

$$\frac{\begin{array}{c} \mathtt{i} \in \{2, 3\} \\ \mathrm{T_1} \leq \mathrm{T_i} \mid \mathcal{C} \cup \{\mathrm{T_1} \leq \mathrm{T_2} \vee \mathrm{T_3}\} \end{array}}{\mathrm{T_1} \leq \mathrm{T_2} \vee \mathrm{T_3} \mid \mathcal{C}} \qquad \text{(S-UNION1)}$$

$$\frac{\begin{array}{c} \mathrm{T_2} \leq \mathrm{T_1} \mid \mathcal{C} \cup \{\mathrm{T_2} {\vee} \mathrm{T_3} \leq \mathrm{T_1}\} \\ \mathrm{T_3} \leq \mathrm{T_1} \mid \mathcal{C} \cup \{\mathrm{T_2} {\vee} \mathrm{T_3} \leq \mathrm{T_1}\} \end{array}}{\mathrm{T_2} {\vee} \mathrm{T_3} \leq \mathrm{T_1} \mid \mathcal{C}} \qquad \text{(S-UNION2)}$$

$$\frac{\begin{array}{c} \mathrm{T} = \{\mathrm{T_1}\ \mathtt{f_1} \ldots, \mathrm{T_i} \vee \mathrm{T_i'}\ \mathtt{f_i} \ldots, \mathrm{T_n}\ \mathtt{f_n}\} \\ \{\mathrm{T_1}\ \mathtt{f_1} \ldots, \mathrm{T_i}\ \mathtt{f_i} \ldots, \mathrm{T_n}\ \mathtt{f_n}\} \leq \mathrm{S} \mid \mathcal{C} \cup \{\mathrm{T} \leq \mathrm{S}\} \\ \{\mathrm{T_1}\ \mathtt{f_1} \ldots, \mathrm{T_i'}\ \mathtt{f_i} \ldots, \mathrm{T_n}\ \mathtt{f_n}\} \leq \mathrm{S} \mid \mathcal{C} \cup \{\mathrm{T} \leq \mathrm{S}\} \end{array}}{\mathrm{T} \leq \mathrm{S} \mid \mathcal{C}} \qquad \text{(S-UNION3)}$$

Figure 2: Subtype rules for Featherweight Whiley. There is no need for an explicit transitivity rule.

Definition 3 (Type Equivalence) *Two types* $\mathrm{T_1}$ *and* $\mathrm{T_2}$ *are said to be* equivalent, *denoted by* $\mathrm{T_1} \equiv \mathrm{T_2}$, *if* $\mathrm{T_1} \models \mathrm{T_2}$ *and* $\mathrm{T_2} \models \mathrm{T_1}$.

Under this definition, two types are equivalent if they define the same regular tree language. Interesting cases arise when we consider *distributivity* of types. Intuitively, $\{\mathtt{int} \vee \mathtt{null}\ \mathtt{f}\} \equiv \{\mathtt{int}\ \mathtt{f}\} \vee \{\mathtt{null}\ \mathtt{f}\}$. However, care must be taken as, for example $[\mathtt{int} \vee \mathtt{null}] \not\equiv [\mathtt{int}] \vee [\mathtt{null}]$ since $[\mathtt{int} \vee \mathtt{null}] \models [1, \mathtt{null}]$, but $[\mathtt{int}] \vee [\mathtt{null}] \not\models [1, \mathtt{null}]$. As another example, consider:

```
define List1 as { int|null dat, null|List1 nxt }
define List2 as { null dat, null|List2 nxt }
define List3 as { int dat, null|List3 nxt }
```

Again, $\mathtt{List1} \not\equiv \mathtt{List2} \vee \mathtt{List3}$ as, if $\mathrm{V} = \{\mathtt{dat} : \mathtt{null}, \mathtt{nxt} : \{\mathtt{dat} : 1, \mathtt{nxt} : \mathtt{null}\}\}$, then $\mathtt{List1} \models \mathrm{V}$ but $\mathtt{List2} \not\models \mathrm{V}$ and $\mathtt{List3} \not\models \mathrm{V}$.

3.2 Subtyping

Whilst the above definitions characterise types in FW, they are not constructive from an implementation perspective. As our primary goal is to develop an efficient implementation, it is important to consider such algorithmic issues. For example, consider:

```
define Link as {int dt, LinkedList nxt}
define LinkedList as null | Link
```

One possible type for `LinkedList` is $\mu X.(\texttt{null} \vee \{\texttt{int dt}, X \texttt{ nxt}\})$, whilst another (equivalent) possibility is $\texttt{null} \vee \{\texttt{int dt}, \mu X.(\texttt{null} \vee \{\texttt{int dt}, X \texttt{ nxt}\}) \texttt{ nxt}\}$. Under Definition 3, recursive types are equivalent to their (infinite) set of unfoldings:

$$
\begin{aligned}
&\mu X.(\texttt{null} \vee \{\texttt{int dt}, X \texttt{ nxt}\}) \\
&\equiv (\texttt{null} \vee \{\texttt{int dt}, \mu X.(\texttt{null} \vee \{\texttt{int dt}, X \texttt{ nxt}\}) \texttt{ nxt}\}) \\
&\equiv (\texttt{null} \vee \{\texttt{int dt}, (\texttt{null} \vee \{\texttt{int dt}, \mu X.(\ldots) \texttt{ nxt}\}) \texttt{ nxt}\}) \\
&\equiv \ldots
\end{aligned}
$$

A critical question is: *how do we implement subtyping efficiently in the presence of such types?*

Amadio and Cardelli were the first to show that subtyping in the presence of recursive types was decidable [29]. Their system included function types, $\top$ and $\bot$. Kozen *et al.* improved this by developing an $\mathcal{O}(n^2)$ algorithm [30]. The system presented here essentially extends this in a straightforward manner. Gapeyev *et al.* give an excellent overview of the subject [28] and, indeed, our subtype relation is identical to theirs, except for the inclusion of unions and other compound structures.

In a nominal type system, types correspond to *trees* and, thus, the subtype operator can be defined using rules such as:

$$\frac{\texttt{T}_1 \leq \texttt{T}_2}{[\texttt{T}_1] \leq [\texttt{T}_2]}$$

Here, a strong property holds that the "height" of $\texttt{T}_1$ is strictly less than $[\texttt{T}_1]$ — leading to a simple proof of termination since every type has finite height. In a structural type system, like FW, types correspond to graphs not trees. Defining the subtype operator using rules such as above leads to non-termination in the presence of cycles. To resolve this we employ ideas from co-induction [28].

The subtyping rules for FW are given in Figure 2. These employ judgements of the form "$\texttt{T}_1 \leq \texttt{T}_2 \downharpoonleft \mathcal{C}$", which are read as: $\texttt{T}_1$ is a subtype of $\texttt{T}_2$ under assumptions $\mathcal{C}$. Essentially, the set of assumptions $\mathcal{C}$ helps ensure the subtype operator terminates (we'll return to this shortly). Ignoring the issue of assumption sets, the rules of Figure 2 are mostly straightforward. Since lists have value semantics, $[\texttt{int}] \leq [\texttt{any}]$ holds. Subtyping of records allows for *depth* and *width* [32]. Thus, $\{\texttt{int x}, \texttt{int y}\} \leq \{\texttt{any x}\}$ by S-REC.

Rule S-UNION3 captures distributivity over records. For example, under S-UNION3, it holds that $\{\texttt{int} \vee \texttt{null x}\} \leq \{\texttt{int x}\} \vee \{\texttt{null x}\}$. The following derivation illustrates a more complex example:

$$
\begin{aligned}
\texttt{T}_1 &= \{\texttt{int} \vee \texttt{null x}, \texttt{int} \vee \texttt{null y}\} \\
\texttt{S}_1 &= \{\texttt{int x}, \texttt{int y}\} \vee \{\texttt{null x}, \texttt{int y}\} \vee \{\texttt{int x}, \texttt{null y}\} \vee \{\texttt{null x}, \texttt{null y}\}
\end{aligned}
$$

1.	$\{\texttt{int x}, \texttt{int y}\} \leq \{\texttt{int x}, \texttt{int y}\} \downharpoonleft \{\ldots\}$	(S-REFLEX)
2.	$\{\texttt{null x}, \texttt{int y}\} \leq \{\texttt{null x}, \texttt{int y}\} \downharpoonleft \{\ldots\}$	(S-REFLEX)
3.	$\{\texttt{int x}, \texttt{null y}\} \leq \{\texttt{int x}, \texttt{null y}\} \downharpoonleft \{\ldots\}$	(S-REFLEX)
4.	$\{\texttt{null x}, \texttt{null y}\} \leq \{\texttt{null x}, \texttt{null y}\} \downharpoonleft \{\ldots\}$	(S-REFLEX)
5.	$\{\texttt{int x}, \texttt{int y}\} \leq \texttt{S}_1 \downharpoonleft \{\ldots\}$	(S-UNION1, 1)
6.	$\{\texttt{null x}, \texttt{int y}\} \leq \texttt{S}_1 \downharpoonleft \{\ldots\}$	(S-UNION1, 2)
7.	$\{\texttt{int x}, \texttt{null y}\} \leq \texttt{S}_1 \downharpoonleft \{\ldots\}$	(S-UNION1, 3)
8.	$\{\texttt{null x}, \texttt{null y}\} \leq \texttt{S}_1 \downharpoonleft \{\ldots\}$	(S-UNION1, 4)
9.	$\{\texttt{int} \vee \texttt{null x}, \texttt{int y}\} \leq \texttt{S}_1 \downharpoonleft \{\ldots\}$	(S-UNION3, 5+6)
10.	$\{\texttt{int} \vee \texttt{null x}, \texttt{null y}\} \leq \texttt{S}_1 \downharpoonleft \{\ldots\}$	(S-UNION3, 7+8)
11.	$\texttt{T}_1 \leq \texttt{S}_1 \downharpoonleft \emptyset$	(S-UNION3, 9+10)

Given the rules of Figure 2, we can give an algorithmic interpretation of subtyping in FW:

Definition 4 (Subtyping) *Let* $\texttt{T}_1$ *and* $\texttt{T}_2$ *be types. Then,* $\texttt{T}_1$ *is a subtype of* $\texttt{T}_2$*, denoted* $\texttt{T}_1 \leq \texttt{T}_2$*, iff* $\texttt{T}_1 \leq \texttt{T}_2 \downharpoonleft \emptyset$*.*

To show $\texttt{T}_1$ is a subtype of $\texttt{T}_2$, we use the rules of Figure 2 starting with no assumptions. As we descend the type graph comparing components of $\texttt{T}_1$ and $\texttt{T}_2$ the set $\mathcal{C}$ *always increases*. The S-INDUCT rule is critical here, as it terminates the recursion (by, essentially, treating the assumption set $\mathcal{C}$ as a "visited" set). Furthermore, the size of $\mathcal{C}$ can be bounded as follows: let m (resp. n) be the number of nodes in the type graph of $\texttt{T}_1$ (resp. $\texttt{T}_2$); then, every addition to $\mathcal{C}$ made by a rule of Figure 2 corresponds to a pair (v, w), where v and w are (respectively) nodes in the type graph of $\texttt{T}_1$ and $\texttt{T}_2$ — thus, $|\mathcal{C}|$ is $O(m \cdot n)$. For example, consider the following type definition (where `LinkedList` is defined in §3.1):

```
define AnyList as null | {any dt, AnyList nxt}

AnyList f(LinkedList l):
    return l
```

For this function to be considered type safe, we need to show that $T_1 = \mu X.(\texttt{null} \vee \{\texttt{int dt},X \texttt{ nxt}\})$ is a subtype of $S_1 = \mu X.(\texttt{null} \vee \{\texttt{any dt},X \texttt{ nxt}\})$:

$$
\begin{aligned}
T_1 &= T_3 \vee T_2 \\
T_2 &= \{\texttt{int dt}, T_1 \texttt{ nxt}\} \\
T_3 &= \texttt{null} \\
S_1 &= S_3 \vee S_2 \\
S_2 &= \{\texttt{any dt}, S_1 \texttt{ nxt}\} \\
S_3 &= \texttt{null}
\end{aligned}
$$

1.	$T_3 \leq S_3 \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-REFLEX)
2.	$\texttt{int} \leq \texttt{any} \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-ANY)
3.	$T_1 \leq S_1 \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-INDUCT)
4.	$T_2 \leq S_2 \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-REC, 2+3)
5.	$T_3 \leq S_1 \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-UNION1, 1)
6.	$T_2 \leq S_1 \downharpoonright \{T_1 \leq S_1\}$	(S-UNION1, 4)
7.	$T_1 \leq S_1 \downharpoonright \emptyset$	(S-UNION2, 5+6)

Here, the assumption $T_1 \leq S_1$ is propagated through the proof, and terminates the recursion at (3) with the use of S-INDUCT.

Finally, the following derivation provides a useful sanity check that $\mu X.\{\texttt{int} \vee \texttt{null data}, \texttt{null} \vee X \texttt{ next}\}$ is *not* a subtype of $\mu X.\{\texttt{int data}, \texttt{null} \vee X \texttt{ next}\} \vee \mu X.\{\texttt{null data}, \texttt{null} \vee X \texttt{ next}\}$:

$$
\begin{aligned}
T_1 &= \{\texttt{int} \vee \texttt{null data}, \texttt{null} \vee T_1 \texttt{ next}\} \\
S_1 &= S_3 \vee S_2 \\
S_2 &= \{\texttt{int data}, \texttt{null} \vee S_2 \texttt{ next}\} \\
S_3 &= \{\texttt{null data}, \texttt{null} \vee S_3 \texttt{ next}\}
\end{aligned}
$$

1.	$\texttt{int} \not\leq \texttt{null} \downharpoonright \{T_1 \leq S_1, \ldots\}$	(By inspection of Fig 2)
2.	$\texttt{null} \not\leq \texttt{int} \downharpoonright \{T_1 \leq S_1, \ldots\}$	(By inspection of Fig 2)
3.	$T_1 \not\leq \texttt{null} \downharpoonright \{T_1 \leq S_1, \ldots\}$	(By inspection of Fig 2)
4.	$\texttt{int} \vee \texttt{null} \not\leq \texttt{null} \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-UNION2, 1)
5.	$\texttt{int} \vee \texttt{null} \not\leq \texttt{int} \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-UNION2, 2)
6.	$T_1 \not\leq S_3 \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-REC, 4)
7.	$T_1 \not\leq S_2 \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-REC, 5)
8.	$T_1 \not\leq \texttt{null} \vee S_3 \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-UNION1, 3,6)
9.	$T_1 \not\leq \texttt{null} \vee S_2 \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-UNION1, 3,7)
10.	$\texttt{null} \vee T_1 \not\leq \texttt{null} \vee S_3 \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-UNION2, 8)
11.	$\texttt{null} \vee T_1 \not\leq \texttt{null} \vee S_2 \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-UNION2, 9)
12.	$\{\texttt{int data}, \texttt{null} \vee T_1 \texttt{ next}\} \not\leq \{\texttt{null data}, \texttt{null} \vee S_3 \texttt{ next}\} \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-REC, 10)
13.	$\{\texttt{int data}, \texttt{null} \vee T_1 \texttt{ next}\} \not\leq \{\texttt{int data}, \texttt{null} \vee S_2 \texttt{ next}\} \downharpoonright \{T_1 \leq S_1, \ldots\}$	(S-REC, 11)
14.	$\{\texttt{int data}, \texttt{null} \vee T_1 \texttt{ next}\} \not\leq S_1 \downharpoonright \{T_1 \leq S_1\}$	(S-UNION1, 12,13)
15.	$T_1 \not\leq S_1 \downharpoonright \emptyset$	(S-UNION3, 14)

Note, in the above, S-UNION2 is used at (10) + (11); however, using S-UNION1 does not affect the outcome.

3.3 Soundness and Completeness

We now provide the necessary connections between the semantic and algorithmic interpretations. Unfortunately, performing a structural induction in the presence of assumption sets is challenging.

Definition 5 (Derivation) *For a given subtyping computation* $T_1 \leq T_2 \downharpoonright C_1$, *let* $\mathcal{D}$ *denote its* derivation. *That is, the set of judgments of the form* $T_3 \leq T_4 \downharpoonright C_2$ *generated by the rules of Figure 2.*

Definition 6 (Projection) *If* $\mathcal{D}$ *is a derivation, then its projection is* $\mathcal{D}^* = \{T_1 \leq T_2 \mid T_1 \leq T_2 \downharpoonright C \in \mathcal{D}\}$.

Lemma 1 (Subtype Assumptions) *Let* T_1 *and* T_2 *be types where computing* $T_1 \leq T_2 \downharpoonright \emptyset$ *generates an intermediate judgment* $T_3 \leq T_4 \downharpoonright C$ *which is shown to hold. If* $T_1 \leq T_2 \downharpoonright \emptyset$ *then* $T_3 \leq T_4 \downharpoonright \emptyset$.

Proof 1 *Let $\mathcal{D}_1$ be the derivation for $T_1 \leq T_2 \downarrow \emptyset$ and $\mathcal{D}_2$ the derivation for $T_3 \leq T_4 \downarrow \mathcal{C}$. By inspection of Figure 2, it follows that $\mathcal{D}_1^* \supseteq \mathcal{D}_2^*$. Let $\mathcal{C}' = \mathcal{C} - \{c\}$ for some arbitrary $c \in \mathcal{C}$, and $\mathcal{D}_3$ be the derivation for $T_3 \leq T_4 \downarrow \mathcal{C}'$. It follows that $\mathcal{D}_1^* \supseteq \mathcal{D}_3^* \supseteq \mathcal{D}_2^*$ as only S-INDUCT is affected by the difference $\mathcal{C}$ and $\mathcal{C}'$. We can also conclude that $\mathcal{D}_3^* = T_3 \leq T_4 \downarrow \mathcal{C}'$ was shown to hold. This is because assumptions do not interfere with the other rules from Figure 2. By an inductive argument, we can conclude $T_3 \leq T_4 \downarrow \emptyset$ is shown to hold.* $\qquad\square$

Theorem 1 (Subtype Soundness) *Let T and T' be types where $T \leq T'$. Then, $T \models T'$.*

Proof 2 *By induction on the structure of T and T'. The induction hypothesis is that if $T_1 \leq T_2 \downarrow \emptyset$ holds for some substructure of T and (resp.) T', then $T_1 \models T_2$. Each case corresponds to a rule from Figure 2, although S-REFLEX, S-VOID, S-ANY are ignored since they follow immediately from Definition 1.*

- *Case $[T_1] \leq [T_2]$: By S-LIST, we have $T_1 \leq T_2 \downarrow \mathcal{C}$ and, hence, $T_1 \leq T_2 \downarrow \emptyset$ by Lemma 1. Thus, it follows from Definition 1 that $[T_1] \models [T_2]$.*

- *Case $\{T_1\ n_1, \ldots, T_m\ n_m\} \leq \{T_1\ T_1, \ldots, T_n\ T_n\}$ where $n < m$: By S-REC, we have $T_i \leq S_i \downarrow \mathcal{C}$ for $1 \leq i \leq m$ and, hence, $T_i \leq S_i \downarrow \emptyset$ by Lemma 1. This follows Definition 1 where only the first n fields of a record are tested for acceptance. Thus, $\{T_1\ n_1, \ldots, T_m\ n_m\} \models \{T_1\ n_1, \ldots, T_n\ n_n\}$.*

- *Case $T_1 \leq T_2 \vee T_3$: By S-UNION1, we have $T_1 \leq T_2 \downarrow \mathcal{C}$ or $T_1 \leq T_3 \downarrow \mathcal{C}$ and, hence, $T_1 \leq T_2 \downarrow \emptyset$ or $T_1 \leq T_3 \downarrow \emptyset$ by Lemma 1. Then $T_1 \models T_2$ or $T_1 \models T_3$. Therefore, $T_1 \models T_2 \vee T_3$ under Definition 1.*

- *Case $T_2 \vee T_3 \leq T_1$: By S-UNION2, we have $T_2 \leq T_1 \downarrow \mathcal{C}$ and $T_3 \leq T_1 \downarrow \mathcal{C}$ and, hence, $T_2 \leq T_1 \downarrow \emptyset$ and $T_3 \leq T_1 \downarrow \emptyset$ by Lemma 1. Then $T_2 \models T_1$ and $T_3 \models T_1$. Therefore, $T_2 \vee T_3 \models T_1$ under Definition 1.*

- *Case $\{T_1\ f_1 \ldots, T_i \vee T_i'\ f_i, \ldots, T_n\ f_n\} \leq T'$: By S-UNION3 and Lemma 1, $\{T_1\ f_1 \ldots, T_i\ f_i, \ldots, T_n\ f_n\} \leq T' \downarrow \emptyset$ and $\{T_1\ f_1 \ldots, T_i'\ f_i, \ldots, T_n\ f_n\} \leq T' \downarrow \emptyset$. Then, $\{T_1\ f_1 \ldots, T_i \vee T_i'\ f_i, \ldots, T_n\ f_n\} \models T'$ under Definition 1.*

- *Case $\overline{T} \to T \leq \overline{S} \to S$: By S-FUN, we have $T \leq S \downarrow \mathcal{C}$ and $\overline{S \leq T \downarrow \mathcal{C}}$ and, hence, $T \leq S \downarrow \emptyset$ and $\overline{S \leq T \downarrow \emptyset}$ by Lemma 1. It follows from Definition 1 that $T \models S$ and $\overline{S \models T}$ Assume $\overline{T} \to T \models \overline{V} \to V$. By Definition 1, we have that $\overline{V \models T}$. Then, it follows that $\overline{V \models S}$. Hence, $\overline{S} \to S \models \overline{V} \to V$.*

$\qquad\square$

Theorem 2 (Subtype Completeness) *Let T and T' be types where $T \models T'$. Then, $T \leq T'$.*

Proof 3 *By induction on the structure of T and T'. The induction hypothesis is that if $T_1 \models T_2$ holds for some substructure of T and (resp.) T', then $T_1 \leq T_2 \downarrow \emptyset$. :*
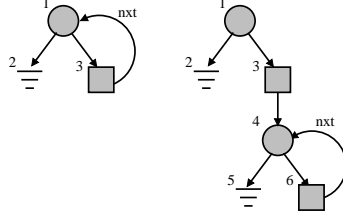
- *Case $\text{void} \models T'$: Straightforward since $\text{void} \leq T'$ by S-VOID.*

- *Case $\text{any} \models T'$: By Definition 1, any accepts every value. Therefore, T' must accept every value and, hence, $T' = \text{any}$. Thus, $\text{any} \leq \text{any}$ by S-REFLEX.*

- *Case $\text{null} \models T'$: By Definition 1, null can only accept value null. Therefore, either $T' \equiv \text{null}$ or $T' \equiv \text{null} \vee T_1$ (for some T_1). Then, $T \leq T'$ by S-NULL and/or S-UNION1.*

- *Case $[T_1] \models [T_2]$: By induction hypothesis, $T_1 \leq T_2$ and, hence, $T \leq T'$ by S-LIST and S-UNION1.*

- *Case $\overline{T} \to T \models \overline{S} \to S$: By Definition 1, $T \models S$ and, by inductive hypothesis, $T \leq S$. It follows from Definition 1 that $\forall \overline{V}.[\overline{V \models T_i} \implies \overline{V \models S_i}]$ (for $1 \leq i \leq n$) and, hence, $\overline{S \models T}$. Finally, $\overline{T \geq S}$ by inductive hypothesis and, hence, $T \leq T'$ by S-FUN.*

- *Case $\{\overline{T\ n}\} \models \{\overline{S\ m}\}$: By Definition 1, $m \leq n$. By induction hypothesis, $\overline{T \leq S}$ and, hence, $T \leq T'$ by S-REC.*

- *Case $T_1 \vee T_2 \models T'$: By Definition 1, $T_1 \models T'$ and $T_2 \models T'$. By induction hypothesis, $T_1 \leq T'$ and $T_2 \leq T'$. Then, $T \leq T'$ by S-UNION2.*

- *Case $[T_1] \models T_2 \vee T_3$: If $T \models T_2$ or $T \models T_3$, then $T \leq T_2 \vee T_3$ by S-UNION1. Assume (for contradiction) that $T \not\models T_2$ and $T \not\models T_3$. It follows from Definition 1 that $T_2 = [T_4]$, $T_3 = [T_5]$ and, hence, $T_1 \models T_4 \vee T_5$. This gives the contradiction as $[T_1]$ will accept a list containing values from both T_4 and T_5 which neither $[T_4]$ nor $[T_5]$ could accept.*

- *Case $\overline{S} \to S \models T_1 \vee T_2$: If $T \models T_1$ or $T \models T_2$, then $T \leq T_1 \vee T_2$ by S-UNION1. Assume (for contradiction) that $T \not\models T_1$ and $T \not\models T_2$. Let $T_1 = \overline{W} \to W$ and $T_2 = \overline{U} \to U$. Then, the contradiction arises in the same manner as for lists. For example, consider return values. It follows from Definition 1 that $S \models W \vee U$. Then, T will accept a function with return values in both W and U which neither T_1 nor T_2 could accept.*

- *Case $\{\overline{S\ f}\} \models T_1 \vee T_2$: If $T \models T_1$ or $T \models T_2$, then $T \leq T_1 \vee T_2$ by S-UNION1. Assume $T \not\models T_1$ and $T \not\models T_2$. Then, some $S_i = T_3 \vee T_4$ must exist. Let $W = \{\overline{W\ f}\}$ where $W_i = T_3$ and $\forall_{j \neq i}.[W_j = S_j]$. Construct W' similarly, except with $W_i' = T_4$. It follows immediately that $W \models T_1 \vee T_2$ and $W' \models T_1 \vee T_2$. Hence, $T \leq T'$ by the inductive hypothesis and S-UNION3.*

$\qquad\square$

3.4 Representation

Another important question, from an implementation perspective, is how types can be represented efficiently. In particular, we desire a compact representation where equivalence testing is efficient[1]. Our starting point is to provide a *graph-based* definition of types:

Definition 7 (Type) *A type is a triple (V, E, K), where V is a set of vertices, E is a set of directed, labelled edges of the form $v \xrightarrow{\ell} w$, and K gives the kind of each vertex (i.e. NULL, INT, LIST, RECORD, UNION, etc).*
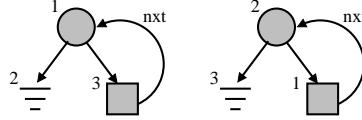
In this definition, the labels ℓ correspond either to field names or ϵ (i.e. no label). Thus, the recursive type $\mu X.(\texttt{null} \vee \{\texttt{int dt}, X \texttt{ nxt}\})$ and its first unfolding can be viewed as follows:

The graph on the right represents a single unfolding of that on the left. Here, circular nodes represent unions, square nodes represent records, and **null** is indicated by ground (for brevity, we omit field dt).

Definition 8 (Minimal Form) *Let $T\!\downarrow$ denote a minimal form of a type T such that, for any $T' \equiv T$, it follows that $|T\!\downarrow| \leq |T'|$.*

To compute $T\!\downarrow$ we can apply standard algorithms for minimising finite automata [49, 50, 51]. Observe that, for any given type, there may exist multiple minimal forms [52]. The following illustrates:

$$
\begin{aligned}
T_1 &= (\{1, 2, 3\}, \{1 \xrightarrow{\epsilon} 2, 1 \xrightarrow{\epsilon} 3, 3 \xrightarrow{nxt} 1\}, \{1 \mapsto \text{UNION}, 2 \mapsto \text{NULL}, 3 \mapsto \text{RECORD}\}) \\
T_2 &= (\{1, 2, 3\}, \{2 \xrightarrow{\epsilon} 3, 2 \xrightarrow{\epsilon} 1, 1 \xrightarrow{nxt} 2\}, \{1 \mapsto \text{RECORD}, 2 \mapsto \text{UNION}, 3 \mapsto \text{NULL}\})
\end{aligned}
$$

Whilst it is clear that $T_1 \equiv T_2$, it is also the case that $T_1 \neq T_2$. Thus, minimising a type does not, by itself, guarantee a canonical form.

Definition 9 (Canonical Form) *Let $\hat{T}$ denote the canonical form of a type T such that, for any T_1 and T_2 where $T_1 \equiv T_2$, it follows that $\hat{T}_1 = \hat{T}_2$.*

This notion of a canonical representation is similar to that found in the study of graph isomorphism, where *canonical labellings* are used by the most efficient algorithms for determining isomorphism [53, 54, 55, 56]. To compute the canonical form of a type T we first minimise it, and then determine a canonical labelling. Intuitively, the idea behind a canonical labelling is to ensure the labels given to vertices are always the same for equivalent types. To get a rough first approximation, we can sort vertices by type. For example, sorting according to NULL < RECORD < UNION gives the following canonical representation of T_1 and T_2:

$$
\begin{aligned}
T_3 &= (\{1, 2, 3\}, \{3 \xrightarrow{\epsilon} 1, 3 \xrightarrow{\epsilon} 2, 2 \xrightarrow{nxt} 3\}, \\
&\quad\ \{1 \mapsto \text{NULL}, 2 \mapsto \text{RECORD}, 3 \mapsto \text{UNION}\})
\end{aligned}
$$

For simplicity, we will not discuss efficient algorithms for computing the canonical representation of a type. However, we will demonstrate that it exists and is always computable by a reduction to coloured graph isomorphism:

Definition 10 (Coloured Digraph) *A coloured digraph is a triple (V, E, C) where V is a set of vertices, E a set of directed edges of the form $v \to w$ and C maps every vertex to a colour taken from $\{1, \ldots, |V|\}$.*

---

[1]This stems from the fact that FW's typing algorithm employs fixed-point iteration and, in practice, this generates a lot of equivalence tests (see §4.4)

Lemma 2 (Canonical Form) *For any type* $\mathtt{T}$*, a canonical form* $\hat{\mathtt{T}}$ *can be computed such that, for any* $\mathtt{T}_1$ *and* $\mathtt{T}_2$ *where* $\mathtt{T}_1 \equiv \mathtt{T}_2$*, it follows that* $\hat{\mathtt{T}}_1 = \hat{\mathtt{T}}_2$*.*

Proof 4 *By reduction to coloured graph isomorphism. W.L.O.G. assume* $\mathtt{T}$ *is minimised. Construct a coloured graph* (V_C, E_C, C_C) *from a type* (V_T, E_T, K_T) *as follows:*

- *Let* $V_C = V_T \cup \{\ell_{vw} \mid v \xrightarrow{\ell} w \in E_T \wedge \ell \neq \epsilon\}$.

- *Let* $E_C = \{v \rightarrow w \mid v \xrightarrow{\epsilon} w \in E_T\} \cup \{v \rightarrow \ell_{vw}, \ell_{vw} \rightarrow w \mid v \xrightarrow{\ell} w \in E_T\}$.

- *Let* $C_C = \{v \mapsto c(k) \mid K_T(v) = k \wedge v \in V_T\} \cup \{\ell_{vw} \mapsto c(\ell) \mid v \xrightarrow{\ell} w \in E_T\}$ *where* $c(\cdot)$ *maps kinds and labels to unique colours.*

Then, a canonical labelling of (V_C, E_C, C_C) *can be computed with well-known algorithms that use back-tracking search to find a minimum lexicographic ordering of edges (e.g. [53, 54]).* $\square$

3.5 Other Operations

An important requirement is that our subtype relation forms a complete lattice. For any two types $\mathtt{T}_1$ and $\mathtt{T}_2$, the least upper bound and greatest lower bound must be well-defined:

Definition 11 *The least upper bound of two types* $\mathtt{T}_1$ *and* $\mathtt{T}_2$*, denoted* $\mathtt{T}_1 \sqcup \mathtt{T}_2$*, is the least type* $\mathtt{T}_3$ *where* $\mathtt{T}_1 \leq \mathtt{T}_3$ *and* $\mathtt{T}_2 \leq \mathtt{T}_3$*.*

Definition 12 *The greatest lower bound of two types* $\mathtt{T}_1$ *and* $\mathtt{T}_2$*, denoted* $\mathtt{T}_1 \sqcap \mathtt{T}_2$*, is the greatest type* $\mathtt{T}_3$ *where* $\mathtt{T}_3 \leq \mathtt{T}_1$ *and* $\mathtt{T}_3 \leq \mathtt{T}_2$*.*

The least upper bound operation is needed to determine variable types at control-flow joins. Computing $\mathtt{T}_1 \sqcup \mathtt{T}_2$ is fairly straightforward as, by construction, it is equivalent to $\mathtt{T}_1 \vee \mathtt{T}_2$. The purpose of the greatest lower bound operation may be less apparent. Consider:

```
define MyType as int | [int] | [[int]]

int f(MyType x):
    if x is [any]:
        return |x|
    else:
        return x
```

The most precise type for x on the true branch is $[\mathtt{int}] \vee [[\mathtt{int}]]$. To update a variable's type after a type test, we *intersect* its type before the test with that used in the test. Or, in other words, we compute their greatest lower bound:

$$(\mathtt{int} \vee [\mathtt{int}] \vee [[\mathtt{int}]]) \sqcap [\mathtt{any}] \implies [\mathtt{int}] \vee [[\mathtt{int}]]$$

Computing $\mathtt{T}_1 \sqcap \mathtt{T}_2$ is only slightly more involved than for $\mathtt{T}_1 \sqcup \mathtt{T}_2$. Recalling the correspondence between types and tree automata from §3.1, then $\mathtt{T}_1 \sqcap \mathtt{T}_2$ corresponds to the *intersection* of the tree automata described by $\mathtt{T}_1$ and that described by $\mathtt{T}_2$. Again, standard algorithms for computing this are known [52].

Another question is how the type of x on the false branch is determined. We use a third operation on types called *greatest difference* (analogous to set difference):

Definition 13 *The greatest difference of* $\mathtt{T}_1$ *and* $\mathtt{T}_2$*, denoted* $\mathtt{T}_1 - \mathtt{T}_2$*, is the greatest type* $\mathtt{T}_3$ *where* $\mathtt{T}_3 \leq \mathtt{T}_1$ *and* $\mathtt{T}_2 \sqcap \mathtt{T}_3 = \mathtt{void}$*.*

Using the greatest difference operation, we obtain the following type for x on the false branch:

$$(\mathtt{int} \vee [\mathtt{int}] \vee [[\mathtt{int}]]) - [\mathtt{any}] \implies \mathtt{int}$$

From this it follows that the last statement of function f() above is type safe since, at this point, x has type **int**. Unfortunately, Definition 13 can only be approximated in FW. For example:

```
int f(any x):
    if x is int:
        ...
    else:
        ...
```

On the false branch, we compute the type $\mathtt{any} - \mathtt{int}$ for x; however, this yields $\mathtt{any}$, since there is no way to encode anything more precise in our language of types. In practice, this does not seem a major concern, as such types are not expressible at the source level.

4 Syntax, Semantics and Typing

Figure 3 gives the syntax for FW. As usual, the overbar (as in e.g. $\overline{S}$) is taken to indicate a list with appropriate separator(s). For simplicity, indentation is not explicitly described in the syntax, but is instead assumed. The notion of a runtime value (given by v) is very similar, but not identical, to that from §3.1. In fact, the only difference lies in the way function values are represented. Finally, for simplicity, we treat direct and indirect invocation with an identical statement. This means direct invocation corresponds to indirect invocation on a constant function value.

4.1 Semantics

A big-step operational semantics for FW is given for expressions in Figure 4, and a small-step semantics for statements in Figure 5. Here, Δ is the *runtime environment*, whilst v, w, u denote *runtime values*. A runtime environment Δ provides access to local variables in the current stack frame, whilst push() and pop() let us (resp.) create and destroy stack frames. Likewise, funDecl(f) gives the declaration of a function f (we do not consider overloading). Finally, halt(v) is taken to mean that the machine has halted, producing value v.

Since function invocation is via a statement, rather than an expression, it follows that expressions have *finite executions* (i.e. they cannot loop indefinitely). This means we can safely employ a big-step semantics for expressions, which simplifies the system (we return to discuss this later). In contrast, we must employ a small-step semantics for statements, as these can loop infinitely (i.e. through **while** loops or recursion).

4.2 Type Environments

Typing functions requires separate environments for each program point. We denote the environment for some point ℓ by Γ^ℓ, which captures the types of all variables immediately before the statement at that point. For example:

```
int f(int x):
    y = x + 1¹
    return y²
```

The typing environments for this function are:

$$\begin{aligned} \Gamma^1 &= \{x \mapsto \texttt{int}\} \\ \Gamma^2 &= \{x \mapsto \texttt{int}, y \mapsto \texttt{int}\} \end{aligned}$$

Since y is undefined before line 1, it is absent from Γ^1. To define how environments are combined at join points in the control-flow graph, we introduce a corresponding subtype relation:

Definition 14 *Let $\Gamma^{\ell 1}$ and $\Gamma^{\ell 2}$ be typing environments. Then, $\Gamma^{\ell 1} \leq \Gamma^{\ell 2}$ iff $\forall v \in \textbf{dom}(\Gamma^{\ell 2}).\left[\Gamma^{\ell 1}[v] \leq \Gamma^{\ell 2}[v]\right]$.*

For example, the following orderings are trivially valid:

$$\begin{aligned} \{v \mapsto \texttt{int}\} &\leq \{v \mapsto \texttt{any}\} \\ \{v \mapsto [\texttt{void}], w \mapsto \texttt{int}\} &\leq \{v \mapsto [\texttt{int}]\} \end{aligned}$$

On the other hand, the following do not hold:

$$\begin{aligned} \{v \mapsto [\texttt{int}]\} &\not\leq \{v \mapsto \texttt{int}\} \\ \{v \mapsto \texttt{int}\} &\not\leq \{v \mapsto \texttt{void}\} \end{aligned}$$

The following illustrates how type environments are combined:

```
int|[int] f(int x):
    if y > 0¹:
        y = [x]²
        z = 2³
    else:
        y = []⁴
    return y⁵
```

The question is, what type does y have in Γ^5? We know that y has $[\texttt{int}]$ or $[\texttt{void}]$ type, and we desire the *most precise environment* capturing this. This corresponds to the *least upper bound* of the type environments involved:

$$\begin{aligned} \Gamma^5 &= \{x \mapsto \texttt{int}, y \mapsto [\texttt{int}], z \mapsto \texttt{int}\} \sqcup \{x \mapsto \texttt{int}, y \mapsto [\texttt{void}]\} \\ &\hookrightarrow \{x \mapsto \texttt{int}, y \mapsto [\texttt{int}]\} \end{aligned}$$

Here, $\Gamma^5[y] = [\texttt{int}]$ as $[\texttt{int}] \vee [\texttt{void}] \equiv [\texttt{int}]$. Furthermore, z cannot be included in Γ^5 as it is not defined on both branches.

Syntax:

$$
\begin{array}{lll}
\texttt{F} & ::= & \texttt{T f}(\overline{\texttt{T n}}) : \texttt{S} \\
\texttt{S} & ::= & \texttt{n} = \texttt{e} \mid \texttt{n.f} = \texttt{e} \mid \texttt{n[e}_1\texttt{]} = \texttt{e}_2 \mid \texttt{n} = \texttt{e}(\overline{\texttt{e}}) \mid \texttt{return e} \mid \texttt{if n is T} : \texttt{S [else S]} \\
& & \mid \texttt{while e}_1 < \texttt{e}_2 : \texttt{S} \mid \texttt{S}_1 ; \texttt{S}_2 \\
\texttt{e} & ::= & \texttt{v} \mid \texttt{n} \mid \texttt{e.f} \mid \texttt{e}_1\texttt{[e}_2\texttt{]} \mid \texttt{[}\overline{\texttt{e}}\texttt{]} \mid \{\overline{\texttt{n} : \texttt{e}}\} \\
\texttt{v} & ::= & \texttt{null} \mid \texttt{i} \mid \texttt{[}\overline{\texttt{v}}\texttt{]} \mid \{\,\overline{\texttt{n} : \texttt{v}}\,\} \mid \texttt{\&f}
\end{array}
$$

Figure 3: Syntax for Featherweight Whiley. Here, $\texttt{n}$ represents variable identifiers, whilst $\texttt{i} \in \mathcal{I}$ represent integer constants.

Expression Reductions:

$$
\frac{\overline{\texttt{e} \rightsquigarrow \texttt{v}}}{\{\overline{\texttt{n} : \texttt{e}}\} \rightsquigarrow \{\overline{\texttt{n} : \texttt{v}}\}}
\qquad
\frac{\overline{\texttt{e} \rightsquigarrow \texttt{v}}}{[\overline{\texttt{e}}] \rightsquigarrow [\overline{\texttt{v}}]}
\qquad
\text{(R-RECORD,}
\text{R-LIST)}
$$

$$
\frac{\begin{array}{c} \texttt{e}_2 \rightsquigarrow \texttt{i} \\ 0 \leq \texttt{i} \leq \texttt{n} \\ \texttt{e}_1 \rightsquigarrow [\texttt{v}_0, \ldots, \texttt{v}_\texttt{n}] \end{array}}{\texttt{e}_1\texttt{[e}_2\texttt{]} \rightsquigarrow \texttt{v}_\texttt{i}}
\qquad
\frac{\begin{array}{c} \texttt{e}_2 \rightsquigarrow \texttt{i} \\ (\texttt{i} < 0 \vee \texttt{i} > \texttt{n}) \\ \texttt{e}_1 \rightsquigarrow [\texttt{v}_0, \ldots, \texttt{v}_\texttt{n}] \end{array}}{\texttt{e}_1\texttt{[e}_2\texttt{]} \rightsquigarrow \textbf{err}}
\qquad
\text{(R-LACCESS1,}
\text{R-LACCESS2)}
$$

$$
\frac{\texttt{e} \rightsquigarrow \{\overline{\texttt{n} : \texttt{v}}\} \quad \texttt{n}_\texttt{i} = \texttt{f}}{\texttt{e.f} \rightsquigarrow \texttt{v}_\texttt{i}}
\qquad
\text{(R-FACCESS)}
$$

Figure 4: Semantics for expressions in FW.

Statement Reductions:

$$
\frac{\Delta \vdash \texttt{e} \rightsquigarrow \texttt{v}}{\langle \Delta, \texttt{n} = \texttt{e} \,;\, \texttt{S} \rangle \longrightarrow \langle \Delta[\texttt{n} \mapsto \texttt{v}], \texttt{S} \rangle}
\qquad \text{(R-VASSIGN)}
$$

$$
\frac{\begin{array}{c} \Delta \vdash \texttt{e} \rightsquigarrow \texttt{v} \\ \Delta(\texttt{n}) = \{\overline{\texttt{n} : \texttt{v}}\} \quad \texttt{v}' = \{\overline{\texttt{n} : \texttt{v}}\}[\texttt{f} \mapsto \texttt{v}] \end{array}}{\langle \Delta, \texttt{n.f} = \texttt{e} \,;\, \texttt{S} \rangle \longrightarrow \langle \Delta[\texttt{n} \mapsto \texttt{v}'], \texttt{S} \rangle}
\qquad \text{(R-FASSIGN)}
$$

$$
\frac{\begin{array}{c} \Delta(\texttt{n}) = [\texttt{u}_0, \ldots, \texttt{u}_\texttt{n}] \quad \Delta \vdash \texttt{e}_2 \rightsquigarrow \texttt{v}_1 \\ \Delta \vdash \texttt{e}_1 \rightsquigarrow \texttt{i} : \texttt{int} \quad 0 \leq \texttt{i} \leq \texttt{n} \\ \texttt{v}_2 = [\texttt{u}_0, \ldots, \texttt{u}_{\texttt{i}-1}, \texttt{v}_1, \texttt{u}_{\texttt{i}+1}, \ldots, \texttt{u}_\texttt{n}] \end{array}}{\langle \Delta, \texttt{n[e}_1\texttt{]} = \texttt{e}_2 \,;\, \texttt{S} \rangle \longrightarrow \langle \Delta[\texttt{n} \mapsto \texttt{v}_2], \texttt{S} \rangle}
\qquad \text{(R-LASSIGN)}
$$

$$
\frac{\Delta_1(\texttt{v}) \in \texttt{T}}{\langle \Delta_1, \texttt{if v is T} : \{\texttt{S}_1\} \texttt{ else } \{\texttt{S}_2\} \,;\, \texttt{S}_3 \rangle \longrightarrow \langle \Delta_2, \texttt{S}_1 \,;\, \texttt{S}_3 \rangle}
\qquad \text{(R-IF1)}
$$

$$
\frac{\Delta_1(\texttt{v}) \notin \texttt{T}}{\langle \Delta_1, \texttt{if v is T} : \{\texttt{S}_1\} \texttt{ else } \{\texttt{S}_2\} \,;\, \texttt{S}_3 \rangle \longrightarrow \langle \Delta_2, \texttt{S}_2 \,;\, \texttt{S}_3 \rangle}
\qquad \text{(R-IF2)}
$$

$$
\frac{\begin{array}{c} \Delta \vdash \texttt{e}_1 \rightsquigarrow \texttt{v}_1 : \texttt{int} \\ \Delta \vdash \texttt{e}_2 \rightsquigarrow \texttt{v}_2 : \texttt{int} \quad \texttt{v}_1 \leq \texttt{v}_2 \end{array}}{\begin{array}{c} \langle \Delta, \texttt{while e}_1 \leq \texttt{e}_2 : \{\texttt{S}_1\} \,;\, \texttt{S}_2 \rangle \\ \longrightarrow \langle \Delta, \texttt{S}_1 ; \texttt{while e}_1 \leq \texttt{e}_2 : \{\texttt{S}_1\} \,;\, \texttt{S}_2 \rangle \end{array}}
\qquad \text{(R-WHILE1)}
$$

$$
\frac{\begin{array}{c} \Delta \vdash \texttt{e}_1 \rightsquigarrow \texttt{v}_1 : \texttt{int} \\ \Delta \vdash \texttt{e}_2 \rightsquigarrow \texttt{v}_2 : \texttt{int} \quad \texttt{v}_1 \not\leq \texttt{v}_2 \end{array}}{\langle \Delta, \texttt{while e}_1 \leq \texttt{e}_2 : \{\texttt{S}_1\} \,;\, \texttt{S}_2 \rangle \longrightarrow \langle \Delta, \texttt{S}_2 \rangle}
\qquad \text{(R-WHILE2)}
$$

$$
\frac{\begin{array}{c} \Delta \vdash \texttt{e} \rightsquigarrow \texttt{\&f} \quad \overline{\texttt{e} \rightsquigarrow \texttt{v}} \\ \texttt{funDecl}(\texttt{f}) = \texttt{T f}(\overline{\texttt{T n}}) : \texttt{S}_2 \\ \Delta_2 = \texttt{push}(\Delta_1, \texttt{S}_1, \texttt{n}) \end{array}}{\langle \Delta_1, \texttt{n} = \texttt{e}(\overline{\texttt{e}}) \,;\, \texttt{S}_1 \rangle \longrightarrow \langle \Delta_2[\overline{\texttt{n} \mapsto \texttt{v}}], \texttt{S}_2 \rangle}
\qquad \text{(R-INVOKE)}
$$

$$
\frac{\Delta_1 \vdash \texttt{e} \rightsquigarrow \texttt{v} \quad \Delta_2, \texttt{S}_2, \texttt{n} = \texttt{pop}(\Delta_1)}{\langle \Delta_1, \texttt{return e} \,;\, \texttt{S}_1 \rangle \longrightarrow \langle \Delta_2[\texttt{n} \mapsto \texttt{v}], \texttt{S}_2 \rangle}
\qquad \text{(R-RETURN)}
$$

$$
\frac{\Delta \vdash \texttt{e} \rightsquigarrow \texttt{v} \quad \epsilon = \texttt{pop}(\Delta)}{\langle \Delta, \texttt{return e} \,;\, \texttt{S} \rangle \longrightarrow \texttt{halt}(\texttt{v})}
\qquad \text{(R-HALT)}
$$

Figure 5: Small-step operational semantics for statements in FW.

Expression Typing:

$$\frac{}{\Gamma \vdash \texttt{null} : \texttt{null}} \qquad \frac{\texttt{i} \in \mathbb{I}}{\Gamma \vdash \texttt{i} : \texttt{int}} \qquad \text{(T-NULL, T-INT)}$$

$$\frac{\mathbf{funType}(\texttt{f}) = \overline{\texttt{T}} \rightarrow \texttt{T}}{\vdash \texttt{\&f} : \overline{\texttt{T}} \rightarrow \texttt{T}} \qquad \frac{\{\texttt{x} \mapsto \texttt{T}\} \in \Gamma}{\Gamma \vdash \texttt{x} : \texttt{T}} \qquad \text{(T-FUN, T-VAR)}$$

$$\frac{\Gamma \vdash \texttt{e}_0 : \texttt{T}_0, \ldots, \Gamma \vdash \texttt{e}_n : \texttt{T}_n}{\Gamma \vdash \{\overline{\texttt{n} : \texttt{e}}\} : \{\overline{\texttt{T n}}\}} \qquad \text{(T-RECORD)}$$

$$\frac{\Gamma \vdash \texttt{e}_0 : \texttt{T}_0, \ldots, \Gamma \vdash \texttt{e}_n : \texttt{T}_n}{\Gamma \vdash [\overline{\texttt{e}}] : [\texttt{T}_0 \vee \ldots \vee \texttt{T}_n]} \qquad \text{(T-LIST)}$$

$$\frac{\Gamma \vdash \texttt{e}_1 : [\texttt{T}] \quad \Gamma \vdash \texttt{e}_2 : \texttt{int}}{\Gamma \vdash \texttt{e}_1 [\texttt{e}_2] : \texttt{T}} \qquad \text{(T-LACCESS)}$$

$$\frac{\Gamma \vdash \texttt{e} : \texttt{T} \quad \mathbf{ert}(\texttt{T}) = \{\ldots, \texttt{T}_i \texttt{ f}, \ldots\}}{\Gamma \vdash \texttt{e.f} : \texttt{T}_i} \qquad \text{(T-FACCESS)}$$

Effective Record Type:

$$\frac{}{\mathbf{ert}(\{\overline{\texttt{T f}}\}) = \{\overline{\texttt{T f}}\}}$$

$$\frac{\begin{array}{c}\texttt{T}_1' = \mathbf{ert}(\texttt{T}_1) = \{\overline{\texttt{T f}}\} \quad \texttt{T}_2' = \mathbf{ert}(\texttt{T}_2) = \{\overline{\texttt{T}' \texttt{f}'}\} \\ \texttt{F} = \mathbf{dom}(\texttt{T}_1') \cap \mathbf{dom}(\texttt{T}_2') \\ \texttt{T}_3 = \{\texttt{T f} \mid \texttt{f} \in \texttt{F} \wedge \texttt{T} = \texttt{T}_1'[\texttt{f}] \vee \texttt{T}_2'[\texttt{f}]\}\end{array}}{\mathbf{ert}(\texttt{T}_1 \vee \texttt{T}_2) = \texttt{T}_3}$$

Figure 6: Typing rules for expressions in Featherweight Whiley.

4.3 Typing Expressions

The typing rules for expressions are given in Figure 6. These are presented using judgements of the form $\Gamma \vdash \texttt{e} : \texttt{T}$, which are taken to mean that under typing environment Γ, expression $\texttt{e}$ has type $\texttt{T}$. Here, $\mathbf{funType}(\texttt{f})$ gives the static type of function $\texttt{f}$.

The *effective record type (ERT)* for type $\texttt{T}$ is given by $\mathbf{ert}(\texttt{T})$. This is necessary for handling unions of records which have fields in common. The following illustrates:

$$\mathbf{ert}\Big(\{\texttt{int x}, [\texttt{int}] \texttt{ dt}\} \vee \{\texttt{any x}\}\Big) = \{\texttt{any x}\}$$
$$\mathbf{ert}\Big(\{\texttt{int x}\} \vee \{[\texttt{int}] \texttt{ x}, \texttt{int cd}\}\Big) = \{(\texttt{int} \vee [\texttt{int}]) \texttt{ x}\}$$
$$\mathbf{ert}\Big(\{\texttt{int x}, [\texttt{int}] \texttt{ dt}\} \vee [\texttt{int}]\Big) = \{\}$$
$$\mathbf{ert}\Big(\mu\texttt{X}.\{\texttt{int x}, (\texttt{X} \vee \texttt{null}) \texttt{ n}\}\Big)$$
$$= \mathbf{ert}\Big(\{\texttt{int x}, (\mu\texttt{X}.\{\texttt{int x}, (\texttt{X} \vee \texttt{null}) \texttt{ n}\} \vee \texttt{null}) \texttt{ n}\}\Big)$$
$$= \{\texttt{int x}, (\mu\texttt{X}.\{\texttt{int x}, ...\} \vee \texttt{null}) \texttt{ n}\}$$

For a union of records, the ERT is formed from those fields common to all by taking the LUB of their types. This identifies those fields (if any) which may be accessed on such a type.

Finally, rule T-LIST from Figure 6 indicates the type of a list constructor is the LUB of all element types:

$$\vdash \texttt{[1,[1]]} \quad : \quad [\texttt{int} \vee [\texttt{int}]]$$
$$\vdash \texttt{[[],[1]]} \quad : \quad [[\texttt{void}] \sqcup [\texttt{int}]] \Longrightarrow [[\texttt{int}]]$$

Using the LUB of the element types ensures we always get the most precise type for a list constructor.

4.4 Typing Statements

As expected, typing statements in FW takes a somewhat different approach than normal. When typing a statement, we need to describe its *effect* on the typing environment. The typing rules are given in Figure 7. These rules are presented as judgements of the form $\Gamma \vdash \texttt{S} : \Gamma'$. Here, Γ represents the typing environment immediately before $\texttt{S}$, whilst Γ' represents that which holds immediately after. Thus, the effect of statement $\texttt{S}$ is captured in the difference between Γ and Γ'. To clarify this, consider the simplest example:

```
int f(string x, string y):
    x = 1¹
    return x²
```

$\Gamma^1 = \{x \mapsto \texttt{string}, y \mapsto \texttt{string}\}$ gives the environment immediately before the assignment. Applying T-VASSIGN from Figure 7 yields the typing environment immediately after it, namely $\Gamma^2 = \{x \mapsto \texttt{int}, y \mapsto \texttt{string}\}$. Thus, the type of x is updated from `string` to `int` by the assignment.

Rule T-FASSIGN from Figure 7 makes use of the function $\mathbf{upert}(T_1, f, T_2)$, which returns an updated version of T_1 where field f now how has type T_2. For unions of records this updates the field across all elements:

```
define LL as {int dt, LL nxt} | null

int f(LL link):
    if link != null¹:
        link.dt = [1]²
        return 0³
    ...
```

The typing environments determined for this function are:

$$\begin{array}{rcl}
\Gamma^1 & = & \{\texttt{link} \mapsto \mu X.(\{\texttt{int dt}, X \texttt{ nxt}\} \vee \texttt{null})\} \\
\Gamma^2 & = & \{\texttt{link} \mapsto \{\texttt{int dt}, \mu X.(\{\texttt{int dt}, X \texttt{ nxt}\} \vee \texttt{null}) \texttt{ nxt}\}\} \\
\Gamma^3 & = & \{\texttt{link} \mapsto \{[\texttt{int}] \texttt{ dt}, \mu X.(\{\texttt{int dt}, X \texttt{ nxt}\} \vee \texttt{null}) \texttt{ nxt}\}\}
\end{array}$$

Here, the **null** test removes the possibility of link being **null** in the true branch. Then, the assignment updates the type of field dt from **int** to [**int**].

Rule T-LASSIGN also behaves somewhat unexpectedly:

```
[int|[int]] f([int] xs):
    if |xs| > 1:
        xs[0] = [1]
    return xs
```

Variable xs has type $[\texttt{int} \vee [\texttt{int}]]$ after the assignment as it now contains an element of $[\texttt{int}]$ type. Of course, there are limitations on how well the type system can reason about list updates:

```
[[int]] f(int y):
    xs = [1]
    xs[0] = [1]
    return xs
```

Whilst xs must hold a value of type $[[\texttt{int}]]$ at the return statement, the type system cannot prove this. The type of xs at this point is (conservatively) determined as $[\texttt{int} \vee [\texttt{int}]]$ and, hence, this program is rejected.

Rule T-RETURN uses **thisFunType** (which holds the enclosing function's type) to check the returned expression satisfies the required return type. The environment determined after a **return** statement is $\bot$ (the subtype of all typing environments). Consider the following:

```
int f(int x):
    if x == 0¹:
        return 0²
    else:
        y = x + 1³
    return y⁴
```

The typing environment which holds before "**return** y" is:

$$\Gamma^4 = \bot \sqcup \{x \mapsto \texttt{int}, y \mapsto \texttt{int}\} \implies \{x \mapsto \texttt{int}, y \mapsto \texttt{int}\}$$

The special environment $\bot$ is needed to ensure y retains its type at this point, since y is not defined on the true branch.

Finally, rule T-WHILE must determine the (least) fixed-point of the typing environment which holds within the body (as is common for flow-sensitive program analysis). This is more complicated than usual as, in some cases, fixed-point iteration is not guaranteed to terminate — we return to discuss this in the following section. This rule is also one reason we're interested in efficient equivalence testing of types (recall §3.4). This is because testing for the fixed-point generates a lot of type equivalence tests.

Statement Typing:

$$\frac{\Gamma_0 \vdash \mathtt{S_1} : \Gamma_1 \quad \Gamma_1 \vdash \mathtt{S_2} : \Gamma_2}{\Gamma_0 \vdash \mathtt{S_1;S_2} :\ \Gamma_2} \qquad \text{(T-SEQ)}$$

$$\frac{\Gamma \vdash \mathtt{e} : \mathtt{T}}{\Gamma \vdash \mathtt{n} = \mathtt{e}\ :\ \Gamma[\mathtt{n} \mapsto \mathtt{T}]} \qquad \text{(T-VASSIGN)}$$

$$\frac{\Gamma \vdash \mathtt{n} : \mathtt{T_1} \quad \Gamma \vdash \mathtt{e} : \mathtt{T_2} \quad \mathbf{ert}(\mathtt{T_1}) = \{\overline{\mathtt{T\ f}}\}}{\Gamma \vdash \mathtt{n.f} = \mathtt{e}\ :\ \Gamma[\mathtt{n} \mapsto \mathbf{upert}(\mathtt{T_1}, \mathtt{f}, \mathtt{T_2})]} \qquad \text{(T-FASSIGN)}$$

$$\frac{\Gamma \vdash \mathtt{n} : [\mathtt{T_1}] \quad \Gamma \vdash \mathtt{e_1} : \mathtt{int} \quad \Gamma \vdash \mathtt{e_2} : \mathtt{T_2}}{\Gamma \vdash \mathtt{n[e_1]} = \mathtt{e_2}\ :\ \Gamma[\mathtt{n} \mapsto [\mathtt{T_1} \vee \mathtt{T_2}]]} \qquad \text{(T-LASSIGN)}$$

$$\frac{\begin{array}{c}\Gamma \vdash \mathtt{e} : \overline{\mathtt{T}} \to \mathtt{T} \\ \hline \Gamma \vdash \mathtt{e} : \mathtt{T'} \quad \mathtt{T'} \le \mathtt{T} \end{array}}{\Gamma \vdash \mathtt{n} = \mathtt{e(\overline{e})}\ :\ \Gamma[\mathtt{n} \mapsto \mathtt{T}]} \qquad \text{(T-INVOKE)}$$

$$\frac{\begin{array}{c}\Gamma \vdash \mathtt{e} : \mathtt{T} \quad \mathtt{T} \le \mathtt{T_r} \\ \hline \mathtt{thisFunType} = \overline{\mathtt{T}} \to \mathtt{T_r} \end{array}}{\Gamma \vdash \mathtt{return\ e}\ :\ \bot} \qquad \text{(T-RETURN)}$$

$$\frac{\begin{array}{c}\Gamma_0 \vdash \mathtt{v} : \mathtt{T_1} \quad \Gamma_3 = \Gamma_1 \sqcup \Gamma_2 \\ \Gamma_0[\mathtt{v} \mapsto \mathtt{T_1} \sqcap \mathtt{T_2}] \vdash \mathtt{S} : \Gamma_1 \quad \Gamma_0[\mathtt{v} \mapsto \mathtt{T_1} - \mathtt{T_2}] \vdash \mathtt{S'} : \Gamma_2 \end{array}}{\Gamma_0 \vdash \mathtt{if\ v\ is\ T_2:\ \{S\}\ else\ \{S'\}} : \Gamma_3} \qquad \text{(T-IF)}$$

$$\frac{\begin{array}{c}\Gamma_0 \sqcup \Gamma_1 \vdash \mathtt{S} : \Gamma_1 \\ \Gamma_0 \sqcup \Gamma_1 \vdash \mathtt{e_1} : \mathtt{int} \quad \Gamma_0 \sqcup \Gamma_1 \vdash \mathtt{e_2} : \mathtt{int} \end{array}}{\Gamma_0 \vdash \mathtt{while\ e_1} \le \mathtt{e_2\ \{S\}} : \Gamma_0 \sqcup \Gamma_1} \qquad \text{(T-WHILE)}$$

Update Effective Record Type:

$$\frac{\mathtt{T_1} = \{\overline{\mathtt{T\ f}}\}}{\mathbf{upert}(\mathtt{T_1}, \mathtt{f_j}, \mathtt{T_2}) = \mathtt{T_1}[\mathtt{f_j} \mapsto \mathtt{T_2}]}$$

$$\frac{\mathtt{T_4} = \mathbf{upert}(\mathtt{T_1}, \mathtt{f}, \mathtt{T_3}) \vee \mathbf{upert}(\mathtt{T_2}, \mathtt{f}, \mathtt{T_3})}{\mathbf{upert}(\mathtt{T_1} \vee \mathtt{T_2}, \mathtt{f}, \mathtt{T_3}) = \mathtt{T_4}}$$

Figure 7: Typing rules for statements in FW.

5 Termination and Soundness

In this section, we prove two important properties for FW, namely: *soundness* and *termination*. Proving termination of FW's typing algorithm depends on showing the type environments $\Gamma_0, \ldots, \Gamma_n$ for rule T-WHILE are monotonically increasing and, hence, always reach a fixed point. Unfortunately, by itself, this is not enough as the lattice has infinite height — however, we show how widening can be used to ensure termination without any loss of precision. Soundness requires that every well-typed program, when given appropriate input, will execute without getting stuck.

5.1 Termination

Showing termination of the typing algorithm relies on showing that we have a join-semi lattice (i.e. of the subtype relation) and that the transfer functions (i.e. the statement typing rules) are monotonic. However, these properties alone are insufficient to show termination of the typing rule T-WHILE from Figure 7. This is because the lattice of types has infinite height and, hence, the fixed-point algorithm may ascend it forever. To address this, we identify the special cases where it may happen, and demonstrate that widening can be employed to ensure termination *without loss of precision*. That is, assuming widening is performed at the correct point, the resulting typing for any program *will be as precise as is possible*.

As an example, consider the following simple FW function:

```
define LoopyList as [int | LoopyList]

LoopyList loopy(int n):
    x = [0]¹
    while |x| < n:
        x[0] = x²
    return x
```

This method will cause a naive fix-point implementation of rule T-WHILE from Figure 7 to iterate forever. The following illustrates the environments that would be produced:

$$
\begin{aligned}
\Gamma^1 &= \{\texttt{n} \mapsto \texttt{int}, \texttt{x} \mapsto [\texttt{int}]\} \\
\Gamma^2 &= \{\texttt{n} \mapsto \texttt{int}, \texttt{x} \mapsto [\texttt{int}]\} \\
\Gamma^2 &= \{\texttt{n} \mapsto \texttt{int}, \texttt{x} \mapsto [\texttt{int} \vee [\texttt{int}]]\} \\
\Gamma^2 &= \{\texttt{n} \mapsto \texttt{int}, \texttt{x} \mapsto [\texttt{int} \vee [\texttt{int} \vee [\texttt{int}]]]\} \\
\Gamma^2 &= \{\texttt{n} \mapsto \texttt{int}, \texttt{x} \mapsto [\texttt{int} \vee [\texttt{int} \vee [\texttt{int} \vee [\texttt{int}]]]]\} \\
&\cdots
\end{aligned}
$$

We can see that the type of $\texttt{x}$ is essentially growing on every iteration of T-WHILE. Informally, we refer to this behaviour as being *divergent* (with respect to typing). Now, the crucial observation is that we can, in fact, give a precise type for $\texttt{x}$ in Γ^2, namely $\mu X.[\texttt{int} \vee X]$. Thus, the algorithm must infer this type from the divergent behaviour, as follows:

$$
\begin{aligned}
\Gamma^2 &= \{\texttt{n} \mapsto \texttt{int}, \texttt{x} \mapsto [\texttt{int} \vee [\texttt{int} \vee [\texttt{int}]]]\} \\
\Gamma^2 &= \{\texttt{n} \mapsto \texttt{int}, \texttt{x} \mapsto [\texttt{int} \vee [\texttt{int} \vee [\texttt{int} \vee [\texttt{int}]]]]\} \\
&\cdots \\
\Gamma^2 &= \{\texttt{n} \mapsto \texttt{int}, \texttt{x} \mapsto \mu X.[\texttt{int} \vee X]\} \\
\Gamma^2 &= \{\texttt{n} \mapsto \texttt{int}, \texttt{x} \mapsto [\texttt{int} \vee \mu X.[\texttt{int} \vee X]]\}
\end{aligned}
$$

At this point, the computation terminates because $\mu X.[\texttt{int} \vee X] \equiv [\texttt{int} \vee \mu X.[\texttt{int} \vee X]]$ under Definition 3. The technique used here is often referred to as *widening* in the program analysis literature (see e.g. [57, 58, 59]) and, generally speaking, results in lost precision. However, in our setting, *widening does not lose precision* (although it is necessary to ensure termination).

Lemma 3 (Monotonicity) *Let* $\Gamma_1 \vdash \texttt{S} : \Gamma_2$ *and* $\Gamma'_1 \vdash \texttt{S} : \Gamma'_2$ *be typing environments. If* $\Gamma_1 \leq \Gamma'_1$*, then* $\Gamma_2 \leq \Gamma'_2$.

Proof 5 *Straightforward by inspection of Figure 7.* □

Definition 15 (Divergence) *Let* $\Gamma_1 \Longmapsto \texttt{S}$ *denote a diverging typing problem where* $\Gamma_1 \vdash \texttt{S} : \Gamma_2, \Gamma_2 \vdash \texttt{S} : \Gamma_3, \ldots$ *is an infinite sequence such that* $\forall \texttt{i}.[\Gamma_\texttt{i} < \Gamma_{\texttt{i}+1}]$.

Lemma 4 (Divergence) *For a diverging typing problem* $\Gamma_1 \Longmapsto \texttt{S}$*, a variable* $\texttt{x}$ *exists where* $\Gamma_\texttt{i}(\texttt{x}) < \Gamma_{\texttt{i}+1}(\texttt{x})$ *occurs infinitely often.*

Proof 6 *Straightforward as, otherwise, computation reaches a fixed point in finitely many steps as we have finitely many variables.* □

Definition 16 *Let* $\texttt{T}_1[\![\texttt{T}_2]\!]$ *indicate type* $\texttt{T}_1$ *contains* $\texttt{T}_2$ *as one (or more) subcomponents.*

Definition 17 (Induction Variable) *A diverging typing problem* $\Gamma_1 \Longmapsto \texttt{S}$ *is said to diverge on a variable* $\texttt{x}$ *if* $\Gamma_\texttt{i}(\texttt{x}) \equiv \texttt{T}[\![\Gamma_\texttt{j}(\texttt{x})]\!]$ *for* $\texttt{i} > \texttt{j}$ *occurs infinitely often.*

Lemma 5 (Induction Variable) *Every diverging typing problem* $\Gamma_1 \Longmapsto \texttt{S}$ *is diverging upon at least one variable* $\texttt{x}$.

Proof 7 *Let* $\texttt{x}_\texttt{i}$ *denote those variables where* $\Gamma_\texttt{i}(\texttt{x}_\texttt{i}) < \Gamma_{\texttt{i}+1}(\texttt{x}_\texttt{i})$ *occurs infinitely often (Lemma 4). Then, there are infinitely many types generated for each* $\texttt{x}_\texttt{i}$*. Furthermore, new types can only be generated from constants, control-flow merges, type tests and through statements of the form* $\texttt{x}_\texttt{i}[\texttt{e}_2] = \texttt{e}_1$ *and* $\texttt{x}_\texttt{i}.\texttt{f} = \texttt{e}_1$*. Since the program structure is fixed, the divergence must stem from one or more statements of the form* $\texttt{x}_\texttt{i}[\texttt{e}_1] = \texttt{e}_2$ *and* $\texttt{x}_\texttt{i}.\texttt{f} = \texttt{e}$*. An infinite number of types can only be generated from such statements through divergence on* $\texttt{x}_\texttt{i}$*.* □

An interesting question is whether or not we can strengthen our notion of an induction variable to say $\Gamma_i(x) \equiv T[\![\Gamma_{i+1}(x)]\!]$ occurs infinitely often. We have found neither counter-example nor proof, but conjecture this is the case. However, one certainly cannot say that $\Gamma_i(x) \equiv T[\![\Gamma_{i+1}(x)]\!]$ for all $i \geq 1$, as the following illustrates:

```
x = [0]
y = 1
while ... :
    x[0] = y
    y = x
```

Here, $\Gamma_1(x) = \Gamma_2(x) = [\mathtt{int}]$. Furthermore, Lemma 5 might appear to fall down on this example:

```
x = [[0]]
while ... :
    x[0][0] = x[0]
```

This is because the key property that $\Gamma_{i+1}(x) = T[\![\Gamma_i(x)]\!]$ does not appear to hold. However, the above example is not valid FW code! Rather, it's translation would be:

```
x = [[0]]
while ... :
    tmp = x[0]
    tmp[0] = x[0]
    x[0] = tmp
```

Thus, in this case, it follows that $\Gamma_2(\mathtt{tmp}) = [\mathtt{int} \vee \Gamma_1(\mathtt{tmp})]$, $\Gamma_3(\mathtt{tmp}) = [\mathtt{int} \vee \Gamma_1(\mathtt{tmp}) \vee \Gamma_2(\mathtt{tmp})]$, etc.

Definition 18 (Least Widening) *Let $\Gamma_1 \Longmapsto S$ be a typing problem which diverges on x. Then, a widening of x is the least type T such that either $\Gamma_1[x \mapsto T] \vdash S : \Gamma_2$ or $\Gamma_1[x \mapsto T] \Longmapsto S$ no longer diverges on x.*

Lemma 6 (Least Widening) *For a typing problem $\Gamma_1 \Longmapsto S$ which diverges on x, there exists a least widening of x.*

Proof 8 *Following Lemma 5 we have $\Gamma_i(x) \equiv T[\![\Gamma_j(x)]\!]$ for $i > j$ infinitely often. Pick the smallest such i and j. Then, by construction, the least widening of x is $\mu X.\Gamma_i(x)[\![\Gamma_j(x) \mapsto X]\!]$.* □

Definition 18 indicates that a diverging typing problem may diverge on multiple independent variables. Widening just one of them does not necessarily eliminate all divergent behaviour. However, we can simply widen each individually until none remains.

We now return to consider the issue of showing that the least fixed point of the typing equations defined in Figure 7 is computable. The essential idea is that, for every non-divergent problem, we can just compute the fixed-point iteratively; but, for a divergent problem we immediately widen the diverging variable(s). The termination theorem states that a least solution to a (possibly diverging) typing problem $\Gamma_1 \vdash S : \Gamma_2$ can be computed:

Theorem 3 (Termination) *Given a typing environment Γ_1, and a statement S, the least solution Γ_2 which satisfies $\Gamma_1 \vdash S : \Gamma_2$ is computable.*

Proof 9 *Straightforward given Lemma 3, Lemma 4 and Lemma 6.* □

5.2 Soundness

In the following Lemmas, we use the term "closed expression" to represent an expression without free variables. Recall from §4.1 that our semantics for expressions given in Figure 4 guarantees finite execution traces. That is, expressions cannot execute indefinitely and will either produce a value, or halt with an error (indicated by **err**):

Lemma 7 (Finite Traces) *Let e_1 be an arbitrary closed expression. Then, either $e_1 \leadsto v$, or $e_1 \leadsto \mathbf{err}$.*

Proof 10 *Straightforward by inspection of Figure 4.* □

Essentially, Lemma 7 justifies our use of a big-step operational semantics for expressions, as it means we do not need to consider the case of non-terminating expressions (which is known to be difficult with a big-step operational semantics, and one important reason for using small-step [60]). As a result, our type-safety proof for expressions does not employ the usual notions of *progress* and *preservation*. Instead, we can prove type-safety directly as follows:

20

Lemma 8 (Effective Record Type) *Let* $\text{ert}(\text{T}) = \{\overline{\text{T f}}\}$ *for some type* T. *Then* $\text{T} \leq \{\overline{\text{T f}}\}$.

Proof 11 *Looking at Figure 6 there are two rules defining* $\text{ert}(\text{T})$. *The case for* $\text{ert}(\{\overline{\text{T n}}\})$ *follows immediately, whilst* $\text{ert}(\text{T}_1 \vee \text{T}_2)$ *follows if we observe that* $\text{T}_1 \leq \text{T}'_1$ *and* $\text{T}_2 \leq \text{T}'_2$ *(by induction), and* $\{\text{T}'_1, \text{T}'_2\} \leq \text{T}_3$. *This latter because* T_3 *includes only fields present in both, and determines the type of each field* f *from the least upper bound of its type in* T'_1 *and* T'_2. $\qquad\square$

Lemma 9 (Expression Safety) *Let* e *be a well-typed, closed expression (i.e.* $\vdash \text{e} : \text{T}_\text{e}$*). Then,* $\text{e} \rightsquigarrow \text{v}$ *where* $\vdash \text{v} : \text{T}_\text{v}$ *and* $\text{T}_\text{v} \leq \text{T}_\text{e}$.

Proof 12 *By induction on the structure of* $\text{e} : \text{T}_\text{e}$. *The T-NULL, T-INT and T-REAL cases are immediate, since* e *in these cases is a value.*

- *Case "*$\{\overline{\text{f} : \text{e}_\text{f}}\}$*": Induction hypothesis guarantees, for each field* f *with initialiser* $\text{e}_\text{f} : \text{T}_\text{f}$*, that* $\text{e}_\text{f} \rightsquigarrow \text{v}_\text{f}$ *where* $\vdash \text{v}_\text{f} : \text{T}'_\text{f}$ *and* $\text{T}'_\text{f} \leq \text{T}_\text{f}$. *Thus,* $\vdash \{\overline{\text{f} : \text{e}_\text{f}}\} \rightsquigarrow \{\overline{\text{f} : \text{v}_\text{f}}\} : \text{T}_\text{v}$ *and so* $\text{T}_\text{v} \leq \text{T}_\text{e}$ *since we have* $\{\overline{\text{T}'_\text{f} \text{ f}}\} \leq \{\overline{\text{T}_\text{f} \text{ f}}\}$ *by S-REC.*

- *Case "*$[\overline{\text{e}}]$*": Induction hypothesis guarantees, for each element expression* $\text{e}_\text{i} : \text{T}_\text{i}$*, that* $\text{e}_\text{i} \rightsquigarrow \text{v}_\text{i}$ *where* $\vdash \text{v}_\text{i} : \text{T}'_\text{i}$ *and* $\text{T}'_\text{i} \leq \text{T}_\text{i}$. *Thus,* $\text{T}_\text{v} \leq \text{T}_\text{e}$ *because* $\text{T}'_1 \sqcup, \ldots, \sqcup \text{T}'_\text{n} \leq \text{T}_1 \sqcup, \ldots, \sqcup \text{T}_\text{n}$ *by Definition 11.*

- *Case "*$\text{e}_1[\text{e}_2]$*": Induction hypothesis guarantees, for source expression* $\text{e}_1 : [\text{T}_\text{e}]$*, that* $\text{e}_1 \rightsquigarrow [\overline{\text{v}}]$ *where* $\vdash [\overline{\text{v}}] : [\text{T}']$ *and* $[\text{T}_\text{v}] \leq [\text{T}_\text{e}]$. *Likewise, for the index expression, it guarantees* $\text{e}_2 \rightsquigarrow \text{i}$ *where* $\vdash \text{i} : \text{int}$. *Thus, assuming the list access is not out-of-bounds, we have* $\text{T}_\text{v} \leq \text{T}_\text{e}$.

- *Case "*$\text{e}_1.\text{f}$*": Induction hypothesis guarantees, for source expression* $\text{e}_1 : \text{T}_1$*, that* $\text{e}_1 \rightsquigarrow \text{v}_1$ *where* $\vdash \text{v}_1 : \{\overline{\text{T}'_\text{f} \text{ f}}\}$ *and* $\{\overline{\text{T}'_\text{f} \text{ f}}\} \leq \text{T}_1$. *T-FACCESS also ensures that* $\text{ert}(\text{T}_1) = \{\overline{\text{T}_\text{f} \text{ f}}\}$ *and, hence, it follows by Lemma 8 that* $\{\overline{\text{T}'_\text{f} \text{ f}}\} \leq \text{T}_1 \leq \{\overline{\text{T}_\text{f} \text{ f}}\}$. *Finally, by S-REC, we have* $\{\overline{\text{T}'_\text{f} \text{ f}}\}.\text{f} \leq \{\overline{\text{T}_\text{f} \text{ f}}\}.\text{f}$ *and, hence,* $\text{T}_\text{v} \leq \text{T}_\text{e}$.

$\qquad\square$

At this point, we can now extend our notion of expression evaluation to include variables from the runtime environment, as follows:

Definition 19 (Expression Evaluation) *Let* Δ *be a runtime environment, and* e *an arbitrary expression with free variables given by* $\text{freeVars}(\text{e}) = \overline{\text{x}}$. *If* $\text{e}[\overline{\text{x} \mapsto \Delta(\text{x})}] \rightsquigarrow \text{v}$ *where* $\text{v} : \text{T}$*, then we say* e *evaluates to a value* v *of type* T *under* Δ*, denoted by* $\Delta \vdash \text{e} \rightsquigarrow \text{v} : \text{T}$.

The following notion of a *safe abstraction* captures the relationship between type environments and their corresponding runtime environments:

Definition 20 (Safe Abstraction) *Let* Γ *be a typing environment and* Δ *a runtime environment. Then,* Γ *is a safe abstraction of* Δ*, denoted* $\Gamma \approx \Delta$*, iff* $\textbf{dom}(\Gamma) \subseteq \textbf{dom}(\Delta)$ *and, for all* $\text{x} \in \textbf{dom}(\Gamma)$ *where* $\Gamma(\text{x}) = \text{T}_\text{x}$*,* $\Delta(\text{x}) = \text{v}$ *and* $\vdash \text{v} : \text{T}_\text{v}$*, it holds that* $\text{T}_\text{v} \leq \text{T}_\text{x}$.

Observe that we cannot require $\textbf{dom}(\Gamma) = \textbf{dom}(\Delta)$, as might be expected, since runtime environments are the product of actual execution paths. Consider an **if** statement with a variable x defined only on one branch. After the statement, $\text{x} \notin \Gamma$ since x was not defined on both branches. However, if execution had proceeded through the branch where x was defined, then we would have $\text{x} \in \Delta$.

Lemma 10 (Safe Join) *Let* $\Gamma_1 \approx \Delta_1$. *Then,* $\Gamma_1 \sqcup \Gamma_2 \approx \Delta_1$ *for any* Γ_2.

Proof 13 *Straightforward, since by construction* $\Gamma_1 \leq \Gamma_1 \sqcup \Gamma_2$ *and, hence,* $\forall \text{x}.[\Gamma_1(\text{x}) \leq (\Gamma_1 \sqcup \Gamma_2)(\text{x})]$ *holds by Definition 14.* $\qquad\square$

Theorem 4 (Progress) *Let* Δ_1 *be a runtime environment and* Γ_1 *a typing environment where* $\Gamma_1 \approx \Delta_1$. *If* $\Gamma_1 \vdash \text{S} : \Gamma_2$*, then either* $\langle \Delta_1, \text{S} ; \text{S}' \rangle \longrightarrow \langle \Delta_2, \text{S}'' \rangle$ *or* $\langle \Delta_1, \text{S} ; \text{S}' \rangle \longrightarrow \text{halt}(\text{v})$.

Proof 14 *By induction on the structure of* S.

- *Case "*$\text{x} = \text{e} ; \text{S}'$*": Since* $\Gamma_1 \vdash \text{S} : \Gamma_2$ *and* $\Gamma_1 \approx \Delta_1$*, it follows by T-VASSIGN that* $\Gamma_1 \vdash \text{e} : \text{T}_\text{e}$ *and, by Lemma 9, that* $\Delta_1 \vdash \text{e} \rightsquigarrow \text{v}$. *Hence,* $\langle \Delta_1, \text{x} = \text{e} ; \text{S}' \rangle \longrightarrow \langle \Delta_1[\text{x} \mapsto \text{v}], \text{S}' \rangle$.

- *Case "x.f = e ; S'": Since* $\Gamma_1 \vdash S : \Gamma_2$ *and* $\Gamma_1 \approx \Delta_1$, *it follows by T-FASSIGN that* $\Gamma_1 \vdash x : T_x$ *where* $\mathrm{ert}(T_x) = \{\overline{T\ f}\}$ *and, hence,* $\Delta_1 \vdash x \rightsquigarrow \{\overline{f : v}\}$ *by Lemma 9. Likewise,* $\Gamma_1 \vdash e : T_e$ *and, by Lemma 9,* $\Delta_1 \vdash e \rightsquigarrow v_1$. *Hence,* $\langle \Delta_1, x.f = e ; S' \rangle \longrightarrow \langle \Delta_1[x \mapsto v_2], S' \rangle$, *where* $v_2 = \{\overline{f : v}\}[f \mapsto v_1]$.

- *Case "x[e_1] = e_2 ; S'": Since* $\Gamma_1 \vdash S : \Gamma_2$ *it follows by T-LASSIGN that* $\Gamma_1 \vdash e_1 : \mathrm{int}$, $\Gamma_1 \vdash e_2 : T_1$ *and* $\Gamma_1 \vdash x : [T_3]$. *Thus,* $\Gamma_1 \approx \Delta_1$ *and Lemma 9 imply that* $\Delta_1 \vdash e_1 \rightsquigarrow v_1 : \mathrm{int}$, $\Delta_1 \vdash e_2 \rightsquigarrow v_2 : T_2$ *and* $\Delta_1 \vdash x \rightsquigarrow v_x$ *(where* $v_x = [\overline{v}]$). *Thus,* $\langle \Delta_1, x[e_1] = e_2 ; S' \rangle \longrightarrow \langle \Delta_1[x \mapsto v'_x], S' \rangle$, *where* $v'_x = v_x[v_1 \mapsto v_2]$.

- *Case "return e ; S'": Since* $\Gamma_1 \vdash S : \Gamma_2$ *and* $\Gamma_1 \approx \Delta_1$, *it follows by T-RETURN that* $\Gamma_1 \vdash e : T_e$ *and, by Lemma 9, that* $\Delta_1 \vdash e \rightsquigarrow v_1$. *Hence,* $\langle \Delta_1, \mathrm{return}\ e ; S' \rangle \longrightarrow \langle \Delta_2, S'' \rangle$ *or* $\langle \Delta_1, \mathrm{return}\ e \rangle \longrightarrow \mathrm{halt}(v)$.

- *Case "if v ~= T: S_1 else: S_2": Since* $\Gamma_1 \vdash S : \Gamma_2$ *and* $\Gamma_1 \approx \Delta_1$, *it follows that* $\Delta_1 \vdash x \rightsquigarrow v_x$. *Thus, either T-IF1 or T-IF2 apply, leading to a transition of the form* $\langle \Delta_1, S \rangle \longrightarrow \langle \Delta_1, S' \rangle$.

- *Case "while* $e_1 \leq e_2$*: S": Since* $\Gamma_1 \vdash S : \Gamma_2$ *it follows by T-WHILE that* $\Gamma_1 \vdash e_1 : \mathrm{int}$ *and* $\Gamma_2 \vdash e_2 : \mathrm{int}$. *Then,* $\Gamma_1 \approx \Delta_1$ *implies that* $\Delta_1 \vdash e_1 \rightsquigarrow v_1 : \mathrm{int}$ *and* $\Delta_1 \vdash e_2 \rightsquigarrow v_2 : \mathrm{int}$ *by Lemma 9. Thus, either R-WHILE1 or R-WHILE2 apply, leading to a transition of the form* $\langle \Delta_1, S \rangle \longrightarrow \langle \Delta_2, S' \rangle$.

- *Case "*$n = e(\overline{e})$*; S'": Since* $\Gamma_1 \vdash S : \Gamma_2$ *and* $\Gamma_1 \approx \Delta_1$, *it follows by T-INVOKE that* $\Delta_1 \vdash e : \overline{T} \to T$, *and that* $\overline{\Delta_1 \vdash e : S}$ *where* $\overline{S \leq T}$. *Hence, by R-INVOKE we have* $\langle \Delta_1, n = e(\overline{e}) ; S' \rangle \longrightarrow \langle \Delta_2, S'' \rangle$.

$\square$

Theorem 5 (Preservation) *Let* $\langle \Delta_1, S \rangle$ *be a runtime environment and* Γ_1 *a typing environment where* $\Gamma_1 \approx \Delta_1$. *If* $\Gamma_1 \vdash S : \Gamma_2$ *for* $S \notin \{n = e(\overline{e}), \mathrm{return}\ e\}$ *and* $\langle \Delta_1, S \rangle \longrightarrow \langle \Delta_2, S' \rangle$, *then* $\Gamma_2 \approx \Delta_2$.

Proof 15 *By induction on the structure of* S.

- *Case "x = e": By T-VASSIGN we have* $\Gamma_2 = \Gamma_1[x \mapsto T_e]$, *where* $\Gamma_1 \vdash e : T_e$. *Similarly, by R-VASSIGN, we have* $\Delta_2 = \Delta_1[x \mapsto v]$ *where* $\Delta_1 \vdash e \rightsquigarrow v : T_v$. *Since* $\Gamma_1 \approx \Delta_1$, *it follows that* $T_v \leq T_e$ *by Lemma 9 and, hence, that* $\Gamma_2 \approx \Delta_2$.

- *Case "x.f = e": By T-FASSIGN we have* $\Gamma_1 \vdash e : T_e$, $\Gamma_1 \vdash x : T_x$ *and* $\Gamma_2 = \Gamma_1[x \mapsto T'_x]$, *where* T'_x *is formed from* T_x *by updating field* f *to* T_e. *Similarly, by R-VASSIGN, we have* $\Delta_2 = \Delta_1[x \mapsto v']$ *where* $\Delta_1 \vdash e \rightsquigarrow v : T_v$, $\Delta_1(x) = \{\overline{n\ :\ v}\}$ *and* $v' = \{\overline{n\ :\ v}\}[f \mapsto v]$. *Since* $T_v \leq T_e$ *by Lemma 9, we have* $\Gamma_2 \approx \Delta_2$.

- *Case "x[e_1] = e_2": By T-LASSIGN we have* $\Gamma_2 = \Gamma_1[x \mapsto [T_1 \sqcup T_2]]$, *where* $\Gamma_1 \vdash x : [T_1]$ *and* $\Gamma_1 \vdash e_2 : T_2$. *By R-LASSIGN,* $\Delta_1 \vdash e_1 \rightsquigarrow i : \mathrm{int}$, $\Delta_1 \vdash e_2 \rightsquigarrow v : T'_2$, $\Delta_1 \vdash x \rightsquigarrow [\overline{v}] : [T'_1]$ *and, since* $\Gamma_1 \approx \Delta_1$, *we have* $T'_1 \leq T_1$ *and* $T'_2 \leq T_2$ *by Lemma 9. Furthermore,* $\Delta_2 = \Delta_1[x \mapsto [\overline{w}]]$ *where* $w_i = v$ *and* $\forall_{j \neq i}.[w_j = v_i]$. *So,* $\Delta_2 \vdash x \rightsquigarrow [\overline{w}] : [T'_1 \sqcup T'_2]$ *and, thus,* $\Gamma_2 \approx \Delta_2$ *since* $T'_1 \sqcup T'_2 \leq T_1 \sqcup T_2$.

- *Case "if v ~= T: S_1 else: S_2": By T-IF we have* $\Gamma_1 \vdash S_1 : \Gamma_3$ *and* $\Gamma_1 \vdash S_2 : \Gamma_4$, *where* $\Gamma_2 = \Gamma_3 \sqcup \Gamma_4$. *Likewise, we have either* $\langle \Delta_1, S_1 \rangle \longrightarrow \langle \Delta_2, \epsilon \rangle$ *by R-IF1 and* $\Gamma_3 \approx \Delta_2$ *(by the induction hypothesis), or* $\langle \Delta_1, S_2 \rangle \longrightarrow \langle \Delta_2, \epsilon \rangle$ *by R-IF2 and* $\Gamma_4 \approx \Delta_2$. *Thus,* $\Gamma_3 \sqcup \Gamma_4 \approx \Delta_2$ *by Lemma 10 and, hence,* $\Gamma_2 \approx \Delta_2$ *(since* $\Gamma_2 = \Gamma_3 \sqcup \Gamma_4$).

- *Case "while* $e_1 \leq e_2$*: S": In executing this loop, the machine goes through a sequence of zero or more transitions of the form* $\langle \Delta_1, S \rangle \longrightarrow \langle \Delta'_1, S \rangle$, $\langle \Delta'_1, S \rangle \longrightarrow \langle \Delta''_1, S \rangle$, *etc. Considering T-WHILE, we arrive at a typing* $\Gamma_1 \sqcup \Gamma_n \vdash S : \Gamma_1 \sqcup \Gamma_n$. *Since* $\Gamma_1 \approx \Delta_1$, *we have by Lemma 10 that* $\Gamma_1 \sqcup \Gamma_n \approx \Delta_1$. *Then, by the induction hypothesis, we get that* $\Gamma_1 \sqcup \Gamma_n \approx \Delta'_1$, $\Gamma_1 \sqcup \Gamma_n \approx \Delta''_1$ *and so on.*

$\square$

Essentially, we limit our preservation theorem to the *intraprocedural* cases as typing is inherently an intraprocedural activity. A coherence theorem handles the movement between functions:

Definition 21 (Invocation) *Let* $\langle \Delta_1, S \rangle \Longmapsto \langle \Delta_2, S' \rangle$ *represent the shortest possible sequence which matches* $\langle \Delta_1, S \rangle \longrightarrow \ldots \longrightarrow \langle \Delta_2, S' \rangle$.

Theorem 6 (Coherence) *Let* $\langle \Delta_1, n = e(\overline{e}) ; S \rangle$ *be a runtime environment and* Γ_1 *a typing environment where* $\Gamma_1 \approx \Delta_1$. *If* $\Gamma_1 \vdash n = e(\overline{e}) : \Gamma_2$ *and* $\langle \Delta_1, n = e(\overline{e}) ; S \rangle \Longmapsto \langle \Delta_2, S \rangle$, *then* $\Gamma_2 \approx \Delta_2$.

Proof 16 *By induction on the structure of the induced call graph. Let* k *indicate the number of function invocations occuring in the sequence* $\langle \Delta_1, n = e(\overline{e}) ; S \rangle \Longmapsto \langle \Delta_2, S \rangle$:

- $k = 0$. *Straightforward as, if no other function invocations occur, then $\Gamma_2 \approx \Delta_2$ essentially follows immediately from Theorem 5.*

- $k = n$. *Consider the first such subsequence $\langle \Delta_3, \mathtt{n} = \mathtt{e}(\bar{\mathtt{e}}) \,; \, \mathtt{S}' \rangle \longmapsto \langle \Delta_4, \mathtt{S}' \rangle$. If Γ_3 and Γ_4 are the typing environments before and after this statement, then $\Gamma_3 \approx \Delta_3$ by Theorem 5, and $\Gamma_4 \approx \Delta_4$ by the induction hypothesis. At this point, we have reduce the problem to the $k - 1$ case.*

$\square$

6 Related Work

In this section, we concentrate primarily on work relating to Whiley's flow-sensitive type system.

The Strongtalk system prioneered the use of structural typing for describing structures from an untyped world (i.e. SmallTalk) [61]. It is perhaps the most closely aligned work to Whiley. The essential idea is succinctly captured in the following quote (from [61]):

> *"Smalltalk is an unusually flexible and expressive language. Any type system for Smalltalk should place a high priority on preserving its essential flavor."*

The paper hints that some form of recursive structural typing was supported, although few details are given and no formalisation is included. Unfortunately, structural subtyping was subsequently dropped from Strongtalk [62], in part due to the complexity of error messages produced. Although not the subject of this paper, we believe the quality of error messages in a structural type system can be improved by storing the names associated with types at their declarations. Then, when reporting an error, the compiler looks up the type in question (or those isomorphic to it) and matches any name(s) in the current file, or imported files.

The work of Guha *et al.* focuses on flow-sensitive type checking for JavaScript [63]. This assumes programmer annotations are given for parameters, and operates in two phases: first, a flow analysis inserts special runtime checks; second, a standard (i.e. flow-insensitive) type checker operates on the modified AST. The system retypes variables as a result of runtime type tests, although only simple forms are permitted. Recursive data types are not supported, although structural subtyping would be a natural fit here; furthermore, the system assumes sequential execution (true of JavaScript), since object fields can be retyped.

Tobin-Hochstadt and Felleisen consider the problem of typing previously untyped Racket (aka Scheme) programs and develop a technique called *occurrence typing* [64]. Their system will retype a variable within an expression dominated by a type test. Like Whiley, they employ union types to increase the range of possible values from the untyped world which can be described; however, they fall short of using full structural types for capturing arbitrary structure. Furthermore, in Racket, certain forms of aliasing are possible, and this restricts the points at which occurrence typing is applicable.

The earlier work of Aiken *et al.* is similar to that of Tobin-Hochstadt and Felleisen [13]. This operates on a function language with single-assignment semantics. They support more expressive types, but do not consider recursive structural types. Furthermore, instead of type checking directly on the AST, conditional set constraints are generated and solved. Following the soft typing discipline, their approach is to insert runtime checks at points which cannot be shown type safe.

The Java Bytecode Verifier requires a flow-sensitive type checker [65]. Since locals and stack locations are untyped in Java Bytecode, it must infer their types to ensure type safety. Like Whiley, the verifier updates the type of a variable after an assignment, and combines types at control-flow join points using a least upper bound operator. However, it does not update the type of a variable after an `instanceof` test. Furthermore, the Java class hierarchy does not form a join semi-lattice. To deal with this, the bytecode verifier uses a simplified least upper bound operator which ignores interfaces altogether, instead relying on runtime checks to catch type errors (see e.g. [66]).

Type qualifiers constrain the possible values a variable may hold. CQual is a flow-sensitive qualifier inference supporting numerous type qualifiers, including those for synchronisation and file I/O [17]. CQual does not account for the effects of conditionals and, hence, retyping is impossible. Fähndrich and Leino discuss a system for checking non-null qualifiers in the context of C# [34]. Here, variables are annotated with `NonNull` to indicate they cannot hold **null**. Non-null qualifiers are interesting because they require variables be retyped after conditionals (i.e. retyping `v` from `Nullable` to `NonNull` after `v!=`**null**). Fähndrich and Leino hint at the use of retyping, but focus primarily on issues related to object constructors. Ekman *et al.* implemented this system within the JustAdd compiler, although few details are given regarding variable retyping [35]. Pominville *et al.* also briefly discuss a flow-sensitive non-null analysis built using SOOT, which does retype variables after `!=`**null** checks [33]. The JACK tool is similar, but focuses on bytecode verification instead [38]. JavaCOP provides an expressive language for writing type system extensions,

including non-null types [43]. This system is flow-insensitive and cannot account for the effects of conditionals; as a work around, the tool allows assignment from a nullable variable `x` to a non-null variable if this is the first statement after a `x!=null` conditional.

Information Flow Analysis is the problem of tracking the flow of information, usually to restrict certain flows for security reasons. The work of Hunt and Sands is relevant here, since they adopt a flow-sensitive approach [18]. Their system is presented in the context of a simple While language not dissimilar to ours, although they do not account for the effect of conditionals. Russo *et al.* use an extended version of this system to compare dynamic and static approaches [67]. They demonstrate that a purely dynamic system will reject programs that are considered type-safe under the Hunt and Sands system. JFlow extends Java with statically checked flow annotations which are flow-insensitive [68]. Finally, Chugh *et al.* developed a constraint-based (flow-insensitive) information flow analysis of JavaScript [69].

Typestate Analysis focuses on flow-sensitive reasoning about the state of objects, normally to enforce temporal safety properties. Typestates are finite-state automatons which can encode usage rules for common APIs (e.g. a file is never read before being opened), and were pioneered by Strom and Yellin [70, 71]. Fink *et al.* present an interprocedural, flow-sensitive typestate verification system which is staged to reduce overhead [72]. Bodden *et al.* develop an interprocedural typestate analysis which is flow-sensitive at the intra-procedural level [73]. This is a hybrid system which attempts to eliminate all failure points statically, but uses dynamic checks when necessary. This was later extended to include a backward propagation step that improves precision [74].

6.1   Type Inference

Type inference is the process of inferring a type for each variable in an untyped (or partially untyped) program. This is generally done by generating and solving either unification- (e.g. [75]) or set-constraints (e.g. [76, 46, 77]) over the program in question.

Numerous type inference systems have been developed for object-oriented languages (e.g. [76, 78, 46, 77, 15, 16]). These, almost exclusively, assume the original program is completely untyped and employ set constraints (see [79]) as the mechanism for inferring types. As such, they address a different problem to that studied here. To perform type inference, such systems generate constraints from the program text, formulate them as a directed graph and solve them using an algorithm similar to transitive closure. When the entire program is untyped, type inference must proceed across method calls (known as *interprocedural analysis*).

Gagnon *et al.* present a technique for converting Java Bytecode into an intermediate representation with a single static type for each variable [80]. Key to this is the ability to infer static types for the local variables and stack locations used in the bytecode. Since local variables are untyped in Java bytecode, this is not always possible as they can — and often do — have different types at different points; in such situations, a variable is split as necessary into multiple variables each with a different type.

Bierman *et al.* formalise the type inference mechanism to be included in C# 3.0, the latest version of the C# language [9]. This uses a very different technique from us, known as *bidirectional type checking*, which was first developed for System F by Pierce and Turner [81]. This approach is suitable for C# 3.0 which does not permit variables to have different types at different program points.

7   Conclusion

We have presented the flow-sensitive and structural type system developed for the Whiley language. This permits variables to be declared implicitly, have multiple types within a function, and be retyped after runtime type tests. The semantics of Whiley also allows flexible typing of updates to compound structures. Additionally, subtyping between user-defined types is implicit. The result is a statically-typed language which, for the most part, has the look and feel of a dynamic language. An open source implementation of Whiley is available from `http://whiley.org`.

We formalised the type system using a core calculus called Featherweight Whiley (FW). We provided both a semantic and algorithmic interpretation of types including a coinductively defined subtype relation. The typing algorithm requires a distinct typing environment at each program point (much like flow-sensitive program analysis). Typing environments are merged at control-flow joins, whilst a greatest lower bound (resp. greatest difference) operator is used for retyping variables on the true (resp. false) branches of runtime type tests. We have sketched soundness and termination properties for this system — Full details can be found in [82].

Finally, there are several improvements to Whiley's type system that could be considered in the future. Most notably, the absence of generic types (i.e. type polymorphism) can result in the need for additional

runtime type tests, compared with dynamically typed languages. The greatest difference operator, as discussed in §3.5, often produces conservative results. This could be resolved by including negation and intersection types [46, 83].

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